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Question Paper Code:U3B04

B.E./B.Tech. DEGREE EXAMINATION, NOV 2023

Third Semester

Biomedical Engineering

21UBM304- SEMICONDUCTOR DEVICES AND CIRCUITS

(Regulations 2021)

Duration: Three hours

Maximum: 100 Marks

Answer All Questions

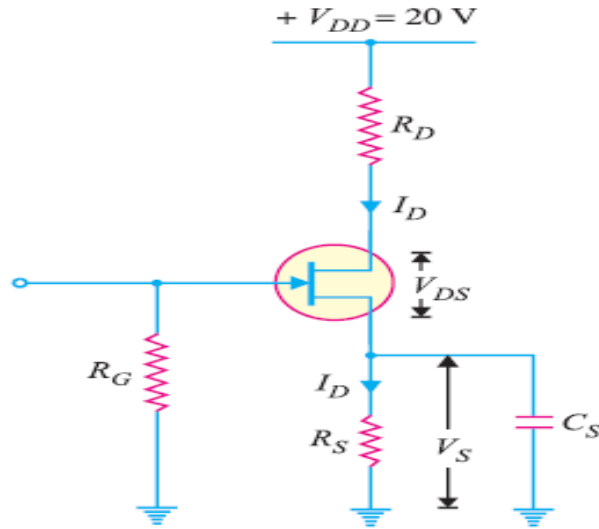
PART A - (10x 2 = 20 Marks)

1. Compare and contrast Zener breakdown and Avalanche breakdown. CO1-U
2. BJT is called current controlled device. Give the reason. CO1-U
3. Why is FET preferred as a Buffer Amplifier? CO1-U
4. Classify the operating region of MOSFET. CO1-U
5. Compare and Contrast CE, CC and CB amplifiers. CO1-U
6. Identify which amplifier is called as voltage follower? Justify your answer. CO1-U
7. List out the application of current shunt feedback amplifier. CO1-U
8. Sketch the diagram for RC Phase shift oscillator. CO1-U
9. Give any two applications of Multivibrators. CO1-U
10. Compare clippers and clampers. CO1-U

PART – B (5 x 16= 80 Marks)

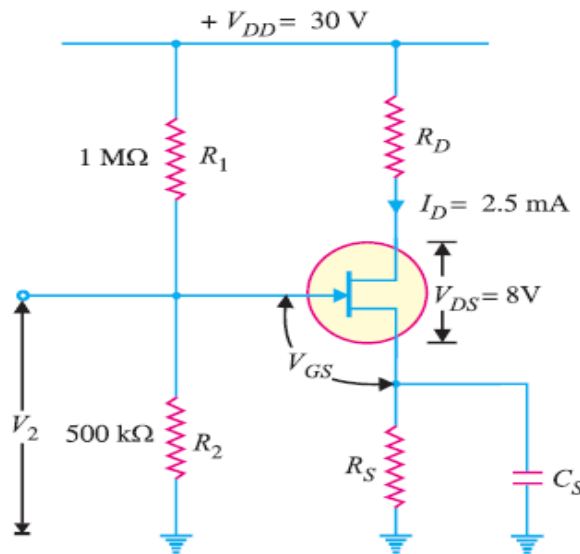
11. (a) (i) Explain the operation of forward biased and reverse biased PN Junction Diode. CO1-U (8)
- (ii) Briefly explain about avalanche and zener breakdown. CO1-U (8)
- Or
- (b) Draw the CE and CC configurations of NPN transistor and explain its input and output characteristics with neat diagram. CO1-U (16)

12. (a) In a self-bias n-channel JFET, the operating point is to be set at $I_D = 1.5 \text{ mA}$ and $V_{DS} = 10 \text{ V}$. The JFET parameters are $I_{DSS} = 5 \text{ mA}$ and $V_{GS}(\text{off}) = -2 \text{ V}$. Find the values of R_S and R_D . Given that $V_{DD} = 20 \text{ V}$. CO2-App (16)



Or

- (b) In an n-channel JFET biased by potential divider method, it is desired to set the operating point at $I_D = 2.5 \text{ mA}$ and $V_{DS} = 8 \text{ V}$. If $V_{DD} = 30 \text{ V}$, $R_1 = 1 \text{ M}\Omega$ and $R_2 = 500 \text{ k}\Omega$, find the value of R_S . The parameters of JFET are $I_{DSS} = 10 \text{ mA}$ and $V_{GS}(\text{off}) = -5 \text{ V}$. CO2-App (16)



13. (a) Examine the small signal equivalent circuit of FET amplifier in CO3-Ana (16)

CB connection and derive the equations for voltage gain, Input Impedance and output impedance.

Or

- (b) Investigate the hybrid model for transistor in Common Base configuration. CO3-Ana (16)
14. (a) Design a RC Phase shift oscillator to generate 5KHz sine wave using BJT with 20V peak to peak amplitude. Draw the desired circuit. Assume $h_{fe}=250$ and $C=1000\text{Pf}$. CO5-C (16)
- Or
- (b) Design a Colpitts Oscillator circuit having two capacitors of 24nF and 240nF respectively are connected in parallel with an inductor of 10mH. Determine the frequency of oscillations of the circuit, the feedback fraction and draw the circuit. CO5-C (16)
15. (a) Explain in brief the construction and working of Free running multivibrator and regenerative comparator with neat diagram. CO1-U (16)
- Or
- (b) (i) Draw and explain the functional diagram of 723 general purpose regulator. CO1-U (8)
- (ii) Explain in detail about clipper and clamper with neat diagram. CO1-U (8)

