A		Reg. No. :											
Question Paper Code: 95303													
B.E. / B.Tech. DEGREE EXAMINATION, NOV 2024													
Fifth Semester													
Electrical and Electronics Engineering													
19UEE503 - POWER SYSTEM ANALYSIS													
(Regulation 2019)													
Duration: Three hours Maximum: 100 Ma Answer ALL Questions								Marks					
PART A - $(10 \text{ x } 1 = 10 \text{ Marks})$													
1.	Stack pointer operates	s in the fast	hion	•								С	01- U
2.	(a) FILO (b) F What is the formula to	× ,	on t	he L	T sec	ction	?		(	(d) L	ILO	C	01- U
	(a) INTR	(b)TRAP.		(c)I	RSTe	5.5.			(d	)RS	Г6.6.		
3.	LXI H,d16 specifies _	addressing	mod	le								С	02- U
	(a) Register	(b)Immediate		(c)I	Indire	ect			(d	) Im	plicit		
4.	What is the required devices in 8051 micro		effic	eient	oper	ation	ı of	seria	al p	ort		C	02- U
	(a) 1200	(b)2400				(	(c)48	800		(	(d) 96	500	
5.	The 8051 has			(a)	4				(	4) 5		С	03- U
6.	(a) 2 On power up, the 805	(b)3 1 uses which RAM lo	ocati		for re	-	er R0	)- R7		d) 5			03- U
7.	(a) 00-2F Which of the follow	(b)00-07 ing is the most con	nmo	· · /	00-7] 1sed		er in	ı the	e ser		(d) 0		7 04- U
	porting?	-									<b>I</b> ( <b>1</b> )		
8.	(a) LIFO How much time perio	(b)FIFO od is necessary for the	e sla	~ /	FILC rece		he ir	nterru	ipt a		(d) L		04- U
	transfer the data? (a) 4 clock time perior	d		(b)	8 clo	ock t	ime j	perio	d				
	(c) 16 clock time period				(d) 24 clock time period								
9.	9. What is the capability of ARM7 f instruction for second?									С	05- U		
	(a) 110 MIPS	(b) 150 MIPS		(c)	125 ]	MIPS	5		(d	) 13(	) MI	PS	

10.		at is the processor used by ARM7? S-bit CISC (b) 8-bit RISC (c) 32-bit CISC PART – B (5 x 2= 10 Marks)	(d) 32-bit H	CO5- U RISC					
11.	What is flag register in 8085 microprocessor? CO1-U								
12.		ine microcontroller and write any two real time applications of rocontroller.	CO2-U						
13.	List	the Interrupts in 8051 Microcontroller	CO3-U						
14.	Wh	at is interrupt service Mechanism?	CO4-U						
15.	Wh	at is RISC?	CO5-U						
PART – C (5 x 16= 80Marks)									
16.	(a)	Explain the timing diagram for opcode fetch and IO write machine cycles with neat diagram	CO1- U	(16)					
	Or								
	(b)	Illustrate the pin outs of 8085 with neat sketch.	CO1- U	(16)					
17.	(a)	Illustrate the architecture of Microcontroller 8051 with functional block diagram	CO2- U	(16)					
		Or							
	(b)	Explain different Addressing Modes of 8051 Microcontroller with examples.	CO2- U	(16)					
18.	(a)	Explain the Timer / Counter functional unit of Microcontroller 8051 with relevant diagrams	CO3- U	(16)					
		Or							
	(b)	Discuss the internal memory organization of 8051 microcontroller	CO3- U	(16)					
19.	(a)	Briefly Explain about I/O Ports, Buses and Interrupt handlers that are embedded in a system	CO4- U	(16)					
		Or							
	(b)	Discuss in detail about design issues in Embedded System	CO4- U	(16)					
20.	(a)	Discuss in detail about the memory organization of PIC micro controller.	CO5- U	(16)					
		Or							

(b) Explain various operating models of ARM, what is coprocessor? and CO5-U (16) how it works. Explain the working of MPU and MMU related memory