С		Reg. No. :											
		Question 3	Paper	Code:	R2	205	5						
B.E./B.Tech. DEGREE EXAMINATION, NOV 2024													
Second Semester													
	Computer Science and Engineering												
R21UCS205- DIGITAL ELECTRONICS													
(Common to Cyber Security Engineering branch)													
(Regulations R2021)													
Dura	ation: Three hours							Max	kimu	ım: 1	00 N	/lark	5
Answer All Questions													
PART A - $(5x 1 = 5 Marks)$													
1.	Hexadecimal Value for	or 15 is equal to										CO	1 - U
	(a) A	(b) B		(c) F					((c) D			
2.	How many AND gates	How many AND gates required for 1 to 8 MUX?							CO2- AP				
	(a) 2	(b) 3		(c) 4					((d) 8			
3.	When both inputs of a	J-K flip-flop cy	cle, the	e output v	will _							CO	1 - U
	(a) Be invalid	(b) Change		(c) Not	chan	ge			((d) T	oggl	e	
4.	In synchronous circuit, the present state is determined by						CO1- U						
	(a)unclocked flip- flops	(b) clocked flops	flip-	(c) flip-	flops	5			((d) la	tche	S	
5.	For programmable log	tic functions, wl	nich typ	e of PLI) sho	ould	be ı	ısed	?			CO	1 - U
	(a) PLA	(b) PAL		(c) CPL	D				((d) S	LD		
PART - B (5 x 3 = 15 Marks)													
6.	Define Number system	n.										CO	1 - U
7.	Define Encoder.											CO	1 - U
8.	What are the different types of flip-flop?							CO1- U					
9.	Define Asynchronous sequential circuit?						CO1 -U						
10.	Define address of a me	emory.										CO	1 - U
PART – C (5 x 16= 80 Marks)													

11.	(a)	Solve the following: i) (1001010.1101001) ₂ to base ₁₀	CO2- AP	(16)			
		ii) $(15.32)_{10}$ to base ₂					
		$(1011DA)_{16}$ to base_{10}					
		Or		$(1 \circ)$			
	(b)	Simplify the expression using k-map	CO2- AP	(16)			
		$F(A,B,C,D) = \sum (0,2,4,6,12,14,15,8,10)$					
12.	(a)	Design Full Adder and derive expression for Sum and Carry in	CO2- AP	(16)			
		Cin(X,y) with circuit diagram.					
		Or					
	(b)	Design a logic circuit that accepts a 4-bit binary code and	CO2- AP	(16)			
		converts it to 4-bit Gray code with input(B3,B2,B1,B0) and					
		output(G3,G2,G1,G0).					
13	(a)	Explain the operation of clocked IK flip-flops with suitable	CO1- U	(16)			
15.	(u)	diagrams?	001 0	(10)			
		Or					
	(b)	Explain the following Shift Registers in detail	CO1- U	(16)			
		a)SIPO b)PISO c)PIPO					
	<i>(</i>)						
14. (a) Explain in o		Explain in detail about asynchronous sequential with neat block	CO1- U	(16)			
		alagram and also discuss design procedure with its suitable					
Or							
	(b)	Explain in detail about Hazards and its types with example?	CO1- U	(16)			
15.	(a)	Explain in detail about the classification of memories with neat	CO1- U	(16)			
		block diagram?					
	A	Or	a a4	(A			
	(b)	Explain the Characteristic function of TTL and CMOS circuits in	CO1- U	(16)			
		Logic families.					