Reg. No. :						

Question Paper Code: U9373

B.E. / B.Tech. DEGREE EXAMINATION, NOV 2024

Open elective

Electrical And Electronics Engineering

21UEE973 - PRINCIPLES OF EMBEDDED COMPUTING SYSTEM

(Common to All branches)

(Regulations 2021)

Duration: Three hours

Maximum: 100 Marks

Answer All Questions

$PART - A (5 \times 20 = 100 \text{ Marks})$

1. (a) How will components be integrated for testing in embedded system CO1-U (20) explain briefly?

Or

- (b) Name two key aspects of platform-level performance analysis and CO1-U (20) briefly describe their significance.
- (a) Explain the features of the LPC 214X microcontroller family, CO2-U (20) focusing on its peripherals such as the timer unit, PWM unit, and UART. Provide examples of programming and using these peripherals in embedded applications

Or

- (b) What is the significance of the duty cycle in a Pulse Width CO2-U (20) Modulation (PWM) signal?
- 3. (a) Explore methods for identifying high-power-consuming CO3-U (20) components, such as processors, peripherals, and memory. What techniques are used to profile and analyze energy consumption at the component level?

Or

(b) Illustrate is the purpose of program validation and testing in CO3-U (20) embedded computing systems?

4. (a) Explain the role of error detection and correction codes in CO4-U (20) maintaining fault tolerance in embedded systems.

Or

- (b) Under the check pointing and rollback techniques enhance fault CO4-U (20) tolerance in embedded systems?
- 5. (a) Name and briefly describe two common IPC mechanisms used in CO5-U (20) embedded systems

Or

(b) How does message passing facilitate communication between CO5-U (20) processes in an embedded computing system?