Question Paper Code: R3205S

B.E./B.Tech. DEGREE EXAMINATION, NOV 2024

Third Semester

Computer Science and Engineering

R21UCS305-COMPUTER ORGANIZATION

(Common to ECE, IT, CSBS, AI&DS, CSD, CSE(AIML), IOT & CYBER SECURITY)

(Regulations R2021)

Duration: Three hours Maximum: 100 Marks

Answer All Questions

PART A - $(10 \times 2 = 20 \text{ Marks})$

1.	What is the straight-line sequencing?			
2.	Define Addressing mode& list out the various types of addressing mode			
3.	Find the 2's complement form for –39,-120.			
4.	What is non –Restoring division?			
5.	What is Data Hazard?			
6.	What is branch folding?			
7.	Define seek time and latency time			
8.	Define hit rate			
9.	How is the interrupt handled during exception?			
10.	What are the three types of buses?			
11.	PART – B (5 x 16= 80 Marks) (a) Explain in detail the various components of computer system CO1-U with neat diagram.	(16)		
	Or			
	(b) What do you mean by addressing modes? Explain various CO1-U addressing modes with the help of an Examples	(16)		
12.	(a) Describe how the floating-point numbers are represented and CO1-U used in digital arithmetic operations. Give an example	(16)		

	(b)	Explain the data path and control unit in detail	CO1-U	(16)
13.	(a)	Define parallel processing and explain the flynn's classification of computer with suitable diagram Or	CO1-U	(16)
	(b)	Explain SPMD and MPP in detail	CO1-U	(16)
14.	(a)	Explain about main memory and its types. Or	CO1-U	(16)
	(b)	What is the use of Translation Look aside Buffers in Virtual Memory organization? With a neat sketch explain the organization of associative mapped TLB.	CO1-U	(16)
15.	(a)	Explain the concept of I/O Systems with necessary diagrams. Or	CO1-U	(16)
	(b)	Describe the working principle of Serial Port in interface circuits.	CO1-U	(16)