

Reg. No. :

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Question Paper Code: R2E05

B.E./B.Tech. DEGREE EXAMINATION, NOV 2024

Second Semester

Artificial Intelligence and Data Science

R21UAD205- DIGITAL LOGIC DESIGN

(Common to CSE(AI&ML) Engineering branches)

(Regulations R2021)

Duration: Three hours

Maximum: 100 Marks

Answer All Questions

PART A - (10 x 2 = 20 Marks)

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|---|---------|
| 1. To Perform Excess-3 Addition for the given number: 16 and 29 | CO2-App |
| 2. What is meant by weighted and non-weighted coding? | CO1-U |
| 3. Write down the applications of Multiplexer | CO1-U |
| 4. Draw the circuit of half-adder. | CO1-U |
| 5. Define Shift Register | CO1-U |
| 6. Write short note on Bistable Multivibrator | CO1-U |
| 7. What do you mean by Race condition? | CO1-U |
| 8. What are the different types of shift type? | CO1-U |
| 9. Differentiate ROM and RAM | CO1-U |
| 10. Define EEPROM | CO1-U |

PART – B (5 x 16= 80 Marks)

11. (a) Reduce the following 4 variable function to its minimum sum of products form: CO2-App (16)

Y

$$= \bar{A}\bar{B}\bar{C}\bar{D} + ABC\bar{D} + \bar{A}\bar{B}C\bar{D} + \bar{A}\bar{B}CD + \bar{A}\bar{B}\bar{C}D + \bar{A}B\bar{C}\bar{D} + \bar{A}\bar{B}CD + \bar{A}\bar{B}\bar{C}\bar{D}$$

Or

