Question Paper Code: 52408 B.E. / B.Tech. DEGREE EXAMINATION, DEC 2021

Second Semester

]	Electronics and Con	nmunication Engineering	5			
		15UEC208 - ELE	CCTRONIC DEVICES				
		(Regul	ation 2015)				
Dur	ation: Three hours			Maximum: 10	0 Marks		
		Answer A	ALL Questions				
		PART A - ($5 \times 1 = 5 \text{ Marks}$				
1.	In a degenerate n type	semiconductor ma	terial, the Fermi level		CO1- R		
	(a) is in valence band						
	(b) is in conduction band						
	(c) is at the centre in between valence and conduction bands						
	(d) is very near valence band						
2.	If a peak rectified voltage for the full-wave filter circuit is 40 V, calculate the filter dc voltage if $C = 75 \mu F$ and load current is 40 mA.						
	(a) 27.9v	(b) 32.12v	(c) 37.78v	(d) 40v			
3.	In which region are forward-biased?	both the collector-b	pase and base-emitter ju-	nctions	CO3- R		
	(a) active	(b) cutoff	(c) saturation	(d) none			
4.	For a JFET, the value is the	of VDS at which II	becomes essentially co	nstant	CO4- R		
	(a) pinch-off voltage	(b) cutoff voltage	(c) breakdown voltage	(d) ohmic voltag	ge		
5.	You need to design a to use might be	relaxation oscillator	r circuit. The most likely	device	CO5- R		
	(a) SCR	(b) UJT	(c) TRIAC	(d) 4-layer diode	e		

$PART - B (5 \times 3 = 15 \text{ Marks})$

6	Define Drift Current.			CO1- R				
7.	Define peak inverse voltage in a PN junction diode			CO2- R				
8.	Define Regulator.			CO3- R				
9.	Draw the structure and symbol for a n-channel JFET			CO4- R				
10.	What is SCR? Mention its Applications.			CO5- R				
$PART - C (5 \times 16 = 80 Marks)$								
11.	(a)	Derive the expression for carrier concentration in intrinsic semiconductor based on structure. Or	CO1- U	(16)				
	(b)	Explain about drift and diffusion currents and obtain its expression.	CO1- U	(16)				
12.	(a)	(i) Explain the operation of PN junction under forward bias condition with its characteristics.	CO2- U	(8)				
		(ii) Explain how a barrier potential is developed at the PN Junction.	CO2- U	(8)				
	Or							
	(b)	Explain the construction and working of full-wave rectifiers and its parameter	CO2- U	(16)				
13.	(a)	With neat diagram explain the operation and input and output characteristics of CE configuration. Or	CO3- U	(16)				
	(b)	A transistor with IB=100 μ A and IC=2mA Find (i) B of the transistor (ii) α of the transistor (iii) emitter current I_E	CO3- U	(16)				
14.	(a)	Explain the construction, working and operating characteristics of N-channel JFET with relevant diagrams. Or	CO4- U	(16)				
	(b)	Explain the principle of operation of enhancement N-channel MOSFET and draw its drain characteristics.	CO4- U	(16)				

15. (a) Draw the VI characteristics of SCR and explain its operation. CO5- U (16) Explain the terms Holding current and latching current

Or

(b) Write short notes on: CO5- U (16)

(i) Photodiode.

(ii) LED

(iii) UJT.