Reg. No.:					

(d) diode

Question Paper Code: 42407

B.E. / B.Tech. DEGREE EXAMINATION, DEC 2021

Second Semester

Electronics and Communication Engineering

14UEC207 - ELECTRONIC DEVICES

(Regulation 2014)

	Duration: Three hours			Maximum: 100 Marks
		Answer ALL	Questions	
		PART A - (10 x 1	1 = 10 Marks)	
	The free electron conce following material	ntration is approxima	ately equal to the dens	sity of donor atoms in
	(a) n - type	(b) p - type	(c) insulator	(d) none of the above
2.	The substance for which called a	ch the width of the f	orbidden energy regi	on is relatively $\sim 1 \mathrm{e} V$ is
	(a) conductor	(b) semiconductor	(c) insulator	(d) none of the above
3.	Which one of the follow	wing is the donor type	e impurity?	
	(a) phosphorous	(b) boron	(c) indium	(d) silicon
4.	For every $10^{\circ}C$ rise in t	emperature the revers	se saturation current a	approximately
	(a) doubles	(b) halves	(c) remains the same	e (d) decreases
5.	By providing proper bia	as voltage ,the transis	tor can be made to w	ork as an

(c) switch

(a) amplifier

(b) regulator

6.	mistakenly. Assur	ming that the amplif	fier of common emi	er terminals get interchanged tter amplifier the biasing is sult into which one of the		
	(a) Zero gain		(b) Infinite	e gain		
	(c) Reduced g	ain	(d) No cha	ange in gain at all		
7.	FET is most usefu	l in chopper circuit be	ecause it has			
	(a) very high i	input impedance	(b) high noise	(b) high noise		
	(c) high offset	voltage	(d) low noise			
8.	FET is a					
	(a) current con	ntrolled device	(b) voltage con	(b) voltage controlled device		
	(c) none of the	e above	(d) both (a) an	(d) both (a) and (b)		
9.	Depends upon the	illumination of fallin	g light its conductivi	ty varies		
	(a) LCD	(b) LED	(c) LDR	(d) photo-diode		
10.	The number of do	ped regions in PIN di	ode is			
	(a) 1	(b) 2	(c) 3	(d) 4		
		PART - B ($5 \times 2 = 10 \text{ Marks}$			
11.		germanium at room t ase per degree rise in	-	By what percent does the		
12.	Draw the circuit d	iagram of a positive of	elamper.			
13.	What is Avalanch	e breakdown?				
14.	14. List any four advantages of FET over conventional transistors.					
15.	Draw the energy b	oand diagrams to show	w the operation of tur	nnel diode.		
		PART - C (S	$5 \times 16 = 80 \text{ Marks}$			
16.	(a) (i) Discuss in	detail about the drift	and diffusion current	. (8)		
	(ii) (1) State I	Mass-Action law.		(4)		
	(2) Calcul	late E_G for S_i and G_e a	t a temperature of 35	$S^{\circ}C$. (4)		

	$\overline{}$	
•	١	r
•	,	

	(b)	(i)	Derive expression of Drift and Diffusion current.	(12)
		(ii)	Write short Notes on Mass action law.	(4)
17.	(a)		ccuss the operation of a PN junction diode under open circuit, forward b erse bias condition.	ias and (16)
		160		(10)
			Or	
	(b)	(i)	With the help of a circuit diagram explain the working of a half-way Also draw the necessary waveforms. Also obtain the expression for factor and efficiency of rectification.	
		(ii)	Show that rectification efficiency for a half wave rectifier is 40.6%.	(4)
18.	(a)	(i)	Explain the construction, circuit representation and working of PNP transistors.	and NPN (8)
		(ii)	Compare the three different configurations of BJT.	(8)
			Or	
	(b)) Dr	aw the block diagram of switched mode power supply and explain each	block
				(16)
19.			scribe the depletion and enhancement mode of working of MOSI e them.	FETS and (16)
			Or	
	(b)	_	plain the principle, structure, operation and characteristics of D-MO MOSFET.	SFET and (16)

20. (a)	Draw and explain the working of SCR and analyze its characteristics with necessary			
	diagrams and waveforms.	(16)		
	Or			
(b)	Write notes on			
	(i) Photo diode	(8)		
	(ii) LED.	(8)		