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Question Paper Code: 58462

B.E. / B.Tech. DEGREE EXAMINATION, DEC 2021

One Credit

Electronics and Communication Engineering

15UEC862 - PCB DESIGN

(Regulation 2015)

Duration: 1.30 hour

Maximum: 50 Marks

Answer ALL Questions

PART A - (30 x 1 = 30 Marks)

- Component placed only on top side called
(a) Multilayer (b) Single layer (c) Double layer (d) Three layer
- _____ Used for soldering
(a) Ferric chloride (b) SiO₂ (c) Ferrous chloride (d) Carbon
- Modern devices are used as
(a) Single layer (b) Double layer (c) Multi layer (d) None of the above
- The component which character is to oppose current flow
(a) Capacitor (b) Resistor (c) Diode (d) Transistor
- A resistor with colour bands Red, Violet, Green and Black will have a value
(a) 27 K \pm 10% K (b) 2.7 M \pm 20% K (c) 270 K \pm 5% K (d) 2.7 K \pm 2% K
- Example of a Photo resistor
(a) LDR (b) Photo diode (c) Diode (d) CFL

16. What kind of paper is used for PCB layout
 (a) Butter flim (b) Normal paper (c) A4 (d) Plastic coated paper
17. If we connect the capacitor in parallel ,it act as
 (a) Storage device (b) Switch (c) Filter (d) Charging/Discharging
18. Which convert light energy into electrical energy?
 (a) LED (b) Photo diode (c) Filter (d) LVDT
19. Which IC give a desired output?
 (a) IC 78XX (b) IC 555 (c) IC 4017 (d) MAX 232
20. Which convert physical energy into electrical energy Vice-Versa?
 (a) Motor (b) Transducer (c) Precipitations (d) OP-AMP
21. The timer IC is
 (a) 555 (b) 444 (c) 222 (d) 777
22. What factors influence PCB fabrication cost
 (a) PCB size (b) Hole diameter (c) Layer count (d) All of the above
23. The most popular form of IC package is
 (a) DIL (b) Flat pack (c) TO-5 (d) None of these
24. The process of removing copper in the pcb designing board is called
 (a) Eracing (b) Enhancing (c) Etching (d) Evoparating
25. Which chemical is used for etching process?
 (a) ferric chloride (b) sulphuric acid
 (c) ferrous sulphate (d) hydrochloric acid
26. The varactor diode is usually
 (a) Forward biased (b) Unbiased (c) reverse biased (d) holes and electronics
27. Which of the following is most difficult to fabricate in an IC?
 (a) Diode (b) Transistor (c) FET (d) Transformer

28. SPST stands for

- (a) single pole single through (b) single polymorised single through
(c) systamatic pole single through (d) silicon polymorised switch

29. In a ac house hold appliance the frequency is in _____ Hz

- (a) 230 (b) 220 (c) 50 (d) 60

30. A rheostat is a form of

- (a) Variable capacitor (b) Potentiometer (c) Variable resistor (d) Thermocouple

PART - B (1 x 20 = 20 Marks)

31. (a) (i) Give types of diodes with necessary diagrams. (8)

(ii) Explain about different types of IC. (12)

Or

(b) (i) Explain about the process of PCB layout design in software with an example. (12)

(ii) Write some software name for design a PCB layout and briefly explain about any one. (8)
