С		Reg. No. :											
	Question Paper Code: 95401												
B.E. / B.Tech. DEGREE EXAMINATION, DEC 2021													
Fifth Semester													
Electronics and Communication Engineering													
19UEC501 - Digital Signal Processing													
(Regulation 2015)													
Dur	ation: Three hours						Max	imum	: 100	Marks			
		Answer ALL	Quest	ions									
PART A - $(5 \times 1 = 5 \text{ Marks})$													
1.	In an N point DFT of a finite duration sequence x(n) of length, the CO1-U value of N should be such that												
	(a) $N > L$ (b) $N \ge L$ (c) $N < L$ (d							(d) N≤	≤L				
2.	The poles of Butterwort	he poles of Butterworth filter lies in s-plane								CO1- U			
	(a) Sphere	(b) Circle	(c) Ellipse (d) Pa						rabol	a			
3.	The frequency response of a digital filter is periodic in the range CO								CO1- U				
	(a) $0 < \omega < 2\pi$		(b) -	· π <	$\omega < \delta$	π							
	(c) $0 < \omega < \pi$		(d) () < 0	0 < 22	πor-	$\pi < \omega$	$< \pi$					
4.	The Finite word length	ffects are due to,								CO1- U			
	(a) Quantization of input	ıt	(b) (Quar	ntizat	ion of	fcoef	ficient	S				
	(c) Quantization of proc	luct	(d) 4										
5.	The total memory space of TMS320C5x family of processors is									CO1- U			
	(a) 224k-words	(b) 224k-bytes	(c) 1	92k-	word	ls	(d)	192k-1	bytes				
		PART – B (5 x 2	3= 15 1	Mark	cs)								
6.	aw the basic butterfly st	erfly structure for radix-2 DIT algorithm? CO1 U											
7.	Determine the order of the butter worth analog filter for the given specification CO2 App												
	$\alpha p = 5$, $\alpha s = 20$, $\Omega p = 10$	000 rad/sec and Ωs	= 500 ı	ad/s	ec								
	- · · · ·												

8.	How the constant group delay and phase delay achieved in linear phase FIR filters?										
9.	What is meant by finite word length effects in digital filters?										
10.	List any two instructions set of TMS320C54x Digital Signal Processors.										
	PART – C (5 x 16= 80 Marks)										
11.	(a)	By means of the DFT and IDFT, determine the response of FIR filter with impulse response $h(n) = \{1,2,3\}$ to the input sequence $x(n) = \{1,2,2,1\}$.	CO2- App	(16)							
	Or										
	(b)	Compute 8-point DFT of the discrete time signal,	CO2- App	(16)							
		$\mathbf{x}(\mathbf{n}) = \{1, 2, 1, 2, 1, 3, 1, 3\}$									
		using Radix-2 DIF FFT.									
12.	(a)	Design a Butter worth digital IIR low pass filter using bilinear transformation by taking T=0.1 sec, to satisfy the following specification.	CO2- App	(16)							
$0.6 \le H(e^{j\omega}) \le 1.0$; for $0 \le \omega \le 0.35\pi$											
$ H(e^{j\omega}) \le 0.1; \text{ for } 0.75\pi \le \omega \le \pi$											
		Or									
	(b)	Design a Chebyshev digital IIR low pass filter using impulse invariant transformation by taking T= 1 sec, to satisfy the following specification.	CO2- App	(16)							
	$0.87 \le H(e^{j\omega}) \le 1.0; \text{ for } 0 \le \omega \le 0.25\pi$										
$ \mathbf{H}(\mathbf{e}^{\mathbf{j}\omega}) \leq 0.35$; for $0.375\pi \leq \omega \leq \pi$											
13.	(a)	Design a linear phase FIR BPF to pass frequency in the range 0.35π to 0.48π rad/sample using rectangular window, by taking 5 samples of window sequence. Analyze the above with Hamming window and comment about the result.	CO3- Ana	(16)							

(b) Design a bandpass filter using frequency sampling method for the CO3- Ana (16) specifications,

Sampling frequency F= 8000Hz

Cutoff frequency $fc_1 = 1000 \text{ Hz}$

fc₂=3000 Hz

Determine the filter coefficients for N=7. If N=5 what will be the filter coefficients?

14. (a) For second-order IIR filter, $H(z) = 1 / (1-0.5z^{-1})(1-0.45z^{-1})$. Study CO2- App (16) the effect of shift in pole location with 3bit coefficient representation in direct and cascade form.

Or

- (b) In the IIR system given below the products are rounded to 4-bits CO2- App (16) (including sign bit). $H(z) = 1 / (1-0.35z^{-1}) (1-0.62z^{-1})$. Find the output round off noise power in a)direct form realization b)cascade realization..
- 15. (a) With a neat functional block diagram, explain the architecture of CO1-U (16) TMS320C5X processor and explain

Or

(b) List the addressing modes of TMS320C5X processor with relevant CO1-U (16) examples.