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Question Paper Code: U2425

B.E./B.Tech. DEGREE EXAMINATION, MAY 2022

Second Semester

Computer Science and Business Systems

| | | 21UEC225- I | Principles Of | f Electronics Er | ngineerir | ng | | |
|------|---|-----------------|---------------|------------------|-----------|------------|-------------|--|
| | | | (Regulation | ons 2021) | | | | |
| Dura | ation: Three hours | | | | | Maximum | : 100 Marks | |
| | | | Answer All | Questions | | | | |
| | | P. | ART A - (5x | 1 = 5 Marks | | | | |
| 1. | If Vdc=Vm/л Vrr | CO3- App | | | | | | |
| | (a) 100% | (b) 90% | | (c) 81.2% | (| (d) 42.5% | | |
| 2. | In an NPN transis | stor, the arrow | is pointed to | owards | | | CO1- U | |
| | (a) the collector | (b) the base | (c) depends | s on the configu | uration | (d) the er | mitter | |
| 3. | The SI Units of th | ne Process tran | sconductanc | e Parameter (k | i') is | | CO1- R | |
| | (a) V2/A | (b) A/V2 | | (c) V/A | | (d) A/V | 7 | |
| 4. | Which of the following gate is called universal gate? | | | | | | CO2- U | |
| | (a) AND | (b) C |)R | (c) XOR | (d |) NAND | | |
| 5. | 5. The truth table for an S-R flip-flop has how many VALID entries? | | | | | | | |
| | (a) 1 | (b) 2 | | (c) 3 | (| (d) 4 | | |
| | | PA | ART - B (5 x | 3= 15 Marks) | | | | |
| 6. | What is meant by | CO1- U | | | | | | |
| 7. | List out the transi | stor H-parame | eters | | | | CO1- U | |
| 8. | Difference between BJT and JFET. | | | | | | CO1- U | |
| 9. | . Implement the half adder using OR gate | | | | | | CO4- App | |
| 10. | What is ripple con | unter? | | | | | CO2- U | |

$PART - C (5 \times 16 = 80 Marks)$

11. (a) A half-wave rectifier, having a resistive load of 1000, rectifies an CO3-App (16)alternating voltage of 325 V peak value and the diode has a forward resistance of 100. Calculate (a) peak, average and rms value of current (b) d.c. power output (c) a.c. input power, and (d) efficiency of the rectifier. Or (b) Compare Half wave, Full wave and Bridge rectifier. CO6-Ana (16)12. (a) Describe the operation and input and output characteristics of CO1-U (16)Emitter follower Or (b) Describe the operation and input and output characteristics of Base CO1-U (16)grounded configuration 13. (a) Explain the construction, working and operating characteristics of CO1-U (16)P-channel JFET with relevant diagrams. Or (b) Explain the principle of operation of enhancement P-channel CO1-U (16)MOSFET and draw its drain characteristics. 14. (a) Design a binary-to-gray code converter and gray to binary code CO4-App (16)converter similar to basic ROM Structure Or (b) Design a binary-to- BCD converter and BCD to binary code CO4-App (16)converter similar to basic ROM Structure (a) Design synchronous up counter for various applications. 15. CO4-App (16)Or

(b) Analyze the use of up /down counter in radar applications

CO5-Ana

(16)