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Question Paper Code: 35502

B.E. / B.Tech. DEGREE EXAMINATION, AUGUST 2021

Fifth Semester

Electronics and Instrumentation Engineering

01UEI502 – MICROPROCESSOR AND INTERFACING

(Regulation 2013)

Duration: 1:45 hour   Maximum: 50 Marks

PART A - (10 x 2 = 20 Marks)

(Answer any ten of the following questions)

1. Differentiate program counter and stack pointer.

2. If the clock frequency is *5MHz*, how much time required executing instructions of 18 T - states?

3. Write the functions of an assembler.

4. What is the function of IO/M signal in the 8085.

5. List the main features of 8259*A* interrupt controller.

6. Mention the applications of *A/D* converters.

7. What is BHE.

8. Define pipelining?

9. Point out the salient features interrupt structure of an 8086 microprocessor.

10. Mention any four flag manipulation instructions.

11. Differentiate program counter and stack pointer.

12. What is the function of the accumulator.

13. Write the functions of an assembler.

14. What is the function of IO/M signal in the 8085.

15. List the main features of 8259*A* interrupt controller.

PART – B (3 x 10= 30 Marks)

(Answer any three of the following questions)

16. Explain the operation of the following 8085 signals: Ready, S1 and S0, HOLD and HLDA and ALE. (10)

17. Explain the direct addressing modes and indirect addressing modes of 8085 with example. (10)

18. Relate the detailed concept of interfacing A/D converter with 8085 processor. (10)

19. Classify the various addressing modes of 8086 microprocessor. (10)

20. Develop a program to add two 8 bit data (*F0H* and *50H*) in 8086 processor and store the result in the memory, when MASM assembler is used. (10)

35502