Reg. No. : | | | | | | | | | | |

Question Paper Code: 44423

B.E. / B.Tech. DEGREE EXAMINATION, AUGUST 2021

Fourth Semester

Computer Science and Engineering

14UEC423 - MICROPROCESSORS AND MICROCONTROLLERS

(Common to Information Technology)

(Regulation 2014)

Duration: 1:45 hour Maximum: 50 Marks

PART A - (10 x 2 = 20 Marks)

(Answer any ten of the following questions)

1. List the flags of 8086 microprocessor.
2. What is the use of ALE?
3. Why status signals are provided in microprocessor?
4. What is the difference between the following instructions of 8086? MOV AX, TABLE_ADDR and LEA AX, TABLE_ADDR.
5. What is the advantage of using 8089 I/O processor instruction of DMS controller?
6. How 8089 operates in loosely coupled configuration and closely coupled configuration?
7. What is a programmable peripheral device?
8. List six modes of timer.
9. Specify the call statement in 8051 with its significance.
10. List the features of 8051 microcontroller.
11. Compare single byte, two byte and three byte instructions.
12. List out the flags present in 8086.

13. Define Bus Arbitration.
14. Highlight the method used to transfer large blocks of data between external device and memory at high speed.
15. List out the priority of 8051 interrupts.

PART – B (3 x 10= 30 Marks)

(Answer any three of the following questions)

16. Describe the Architecture of 8085 with neat explanation. (10)
17. Explain the addressing modes of 8086 with examples. (10)
18. List the various types of coprocessor configurations? Explain them in detail. (10)
19. Show the function of keyboard and display controller with a neat sketch. (10)
20. Draw the architecture of 8051 microcontroller and explain each block. (10)