Reg. No. :						

# **Question Paper Code: U2502**

# M.E. DEGREE EXAMINATION, APRIL / MAY 2025

# Second Semester

#### Power Electronics and Drives

# 21PPE202 - EMBEDDED CONTROL OF POWER ELECTRONIC DRIVE SYSTEMS

(Regulations 2021)

Duration: Three hours Maximum: 100 Marks

# Answer ALL Questions

 $PART - A (5 \times 20 = 100 \text{ Marks})$ 

1. (a) Assume that you are having a robot in your company. The CO2 -App (20) forearm of the robot is set in the upward direction. Make your robot to invite your visitors by giving handshake to them with the use of TMS320C5X Instruction set.

Or

- (b) Implement an assembly language subroutine to read the speed of CO2 -App (20) an electric drive motor using a DSP? Explain the process.
- 2. (a) Explain how the microprocessor handles a situation when it CO1 U (20) receives three interrupts at a time from the interrupt pins INT3\_, RINT0 and RESET.

Or

- (b) Explain how interrupts are used to enhance the safety and CO1 U (20) reliability of power electronic systems.
- 3. (a) Design a 12 bit ADC which is operating with  $1\mu S$  clock period CO3 -App (20) and with total conversion time of  $14 \mu S$ .

OR

(b) Convert an analog input of 6.6V to the corresponding digital CO3 -App (20) output using 4 bit counting ADC and Successive Approximation type ADC. Assume the resolution of ADC is 0.5V

4. (a) Evaluate the cost-effectiveness of deploying FPGA-based versus CO4 -Ana (20) DSP-based solutions for controlling electrical drives, considering factors such as initial hardware costs, development expenses, and long-term maintenance.

OR

- (b) Analyze the impact of the integration of PowerPC processors in CO4 -Ana (20) FPGA devices, such as the Virtex II Pro series, on the performance and versatility of motor control applications.
- 5. (a) Develop VHDL code to generate a control signal for auto CO2 -App (20) tracking solar panel.

OR

(b) Develop the assembly language program to implement a PWM CO2 -App (20) (Pulse Width Modulation) controller for a DC motor using a specific DSP. Explain how you would ensure the accuracy and stability of the PWM signal.