| С | | Reg. No. : | | | | | | | | | | | | |
|---|--|----------------------------------|----------|-------|---------|--------|-------|---------|----|------------|-------|-------|--------------|--|
| | Question Paper Code: R2F05 | | | | | | | | | | | | | |
| B.E./B.Tech. DEGREE EXAMINATION, APRIL 2024 | | | | | | | | | | | | | | |
| Second Semester | | | | | | | | | | | | | | |
| Computer science and Design | | | | | | | | | | | | | | |
| R21UCD205- DIGITAL AND COMPUTER ORGANIZATION | | | | | | | | | | | | | | |
| (Regulations R2021) | | | | | | | | | | | | | | |
| Duration: Three hours Maximum: 100 Marks | | | | | | | | | | | arks | | | |
| Answer All Questions | | | | | | | | | | | | | | |
| PART A - $(5x 1 = 5 Marks)$ | | | | | | | | | | | | | | |
| 1. | What is the addition of the binary number 101001+010011=? | | | | | | | | | CO2-App | | | | |
| | (a) 010100 | (a) 010100 (b) 111100 (c) 000111 | | | | | | | (| (d) 101110 | | | | |
| 2. | How many outputs would two 8:3 line encoders, expanded to a 16:4 line encoder, have? | | | | | | | | :4 | CO1-U | | | 1 - U | |
| | (a) 3 | (b) 4 | | (c) 5 | | | | | (| (d) 6 | | | | |
| 3. | The addressing mode, where you directly specify the operand value is | | | | | | | ue is | | CO1-U | | | | |
| | (a) Immediate | (b) Direct | | (c)D | efinite | e | | | (| (d) R | elati | ve | | |
| 4. | Throughput is calculated as | | | | | | | | | CO1-U | | | | |
| | (a) The number of instructions/ Total time to complete the instructions | | | | | | | | | | | | | |
| | (b) Total time to comp | plete the instruc | ctions/n | umber | ofing | struct | tions | | | | | | | |
| | (c) Speed of the processor/ Number of instructions | | | | | | | | | | | | | |
| | (d) The number of instructions/speed of the processor | | | | | | | | | | | | | |
| 5. | The performance of cache memory is frequently measured in terms of a quantity called | | | | | | a | CO1-U | | | | | | |
| | (a) Miss ratio | (b) Hit ratio | | (c) L | atenc | y rati | io | | (| (d) R | ead 1 | ratio | | |
| PART - B (5 x 3 = 15 Marks) | | | | | | | | | | | | | | |
| 6. | Convert $(1101.101)_2$ to decimal equivalent. | | | | | | | CO2-App | | | | | | |
| 7. | Difference between encoder and decoder. | | | | | | | CO1-U | | | | | | |
| 8. | Define Index addressing Mode. | | | | | | | CO1-U | | | | | | |

| 9. | Wha | at is locality of reference? | CO1-U | | |
|-----|-----|--|---------|------|--|
| 10. | Wha | at are static and dynamic memories? | CO1-U | | |
| | | PART – C (5 x 16= 80 Marks) | | | |
| 11. | (a) | Convert the following:- i) 67110 to binary ii) 110102 to decimal iii) 01011112 to octal iv) 5112 ₁₀ to hexadecimal | CO2-App | (16) | |
| | (h) | (b) Consider the function | | (16) | |
| | (0) | $f(A,B,C,D) = \sum m (0,1,2,3,5,7,8,10,12,13,15)$ using Quine Mccluskey Method. | CO2-App | (16) | |
| 12. | (a) | Implement the following Boolean function using 4 X 1 MUX $F(a, b, c, d) = \sum m(0,1, 3, 4, 8, 9, 15)$. Explain in detail. Or | CO2-App | (16) | |
| | (b) | Implement the following Boolean function using 8:1 multiplexer $F(A,B,C,D)=(A+B+\overline{C}).(\overline{A}+B+\overline{D}).(A+\overline{C}+D).(B+C+D)$ | CO2-App | (16) | |
| 13. | (a) | Write the various types of instructions with an example. Or | CO1-U | (16) | |
| | (b) | Explain various instruction format illustrate the same with an example | CO1-U | (16) | |
| 14. | (a) | Explain Hardware and Booths multiplication technique. Or | CO1-U | (16) | |
| | (b) | What are the various hazards that might arise in a pipeline? Discuss how to reduce or minimize the hazards. | CO1-U | (16) | |
| 15. | (a) | Explain the need for cache memory and discuss the different types of mapping functions with necessary block diagram. Or | CO1-U | (16) | |
| | (b) | Draw the neat sketch and explain the memory hierarchy and Memory technology. | CO1-U | (16) | |