| Reg. No.: |
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Question Paper Code: 45304A

B.E. / B.Tech. DEGREE EXAMINATION, MAY 2018

Fifth Semester

Electrical and Electronics Engineering

14UEE504 - MICROPROCESSORS AND MICROCONTROLLER PROGRAMMING

| 14UEE3U4 - MICKOPKO | JCESSORS AND I | MICROCONTROLL | ER PROGRAMMING | | |
|---|--------------------|--|------------------------|--|--|
| | (Regulation | on 2014) | | | |
| Duration: Three hours | | | Maximum: 100 Marks | | |
| | Answer ALI | Questions | | | |
| | (Polar Graph sheet | s to be provided) | | | |
| | PART A - (10 x | 1 = 10 Marks) | | | |
| 1instruction subroutine sequence | is used to return | to calling progra | m after completing the | | |
| (a) RST | (b) CALL | (c) RET | (d) TRAP | | |
| 2. What are level triggerin | g interrupts? | | | | |
| (a) RST 6.5 and RST5.5 (c) RST 5.5 and RST7.5 | | (b) RST7.5 and RST 6.5(d) INTR and TRAP | | | |
| If 'n' denotes number of clock cycles and 'T' denotes period of the clock at which the microprocessor is running, then duration of execution of loop once can be denoted by | | | | | |
| (a) $n+T$ | (b) <i>n-T</i> | (c) $n*T$ | (d) <i>n/T</i> | | |

4. A general purpose microprocessor requires which of the following device to operate

(c) IO Ports

(d) All of these

(b) RAM

properly

(a) ROM

| 5. | The instruction that microcontroller is | is used to complement | nt the bit of a bit a | ddressable SFR in 8051 | |
|-----|--|--------------------------------------|---|----------------------------|--|
| | (a) CLR C | (b) CPL C | (c) CPL bit | (d) ANL bit | |
| 6. | Which of the follow | ing register can be used | as two individual 8 | bit registers? | |
| | (a) IE | (b) DPTR | (c) TMOD | (d) PSW | |
| 7. | The register that m register and current | _ | py of the respective | e initial current address | |
| | (a) mode register(c) command register | | (b) base address register(d) mask register | | |
| 8. | Intel 8255, under the | e Hand shake I/O mode | of operation, we hav | e modes. | |
| | (a) Mode 0 | (b) Mode 1 | (c) Mode 2 | (d) All of these | |
| 9. | The device that is u of steps is | sed to obtain an accura | te position control o | f rotating shafts in terms | |
| | (a) DC motor | (b) AC motor | (c) Stepper motor | (d) Servo motor | |
| 10. | The internal schema | tic of a typical stepper i | motor has | | |
| | (a) 1 winding | (b) 2 windings | (c) 3 windings | (d) 4 windings | |
| | | PART - B (5 x 2 | 2 = 10 Marks) | | |
| 11. | Differentiate microp | rocessor and microcont | roller. | | |
| 12. | Write the use of AL | E signal. | | | |
| 13. | List the five interrup | ot sources of 8051 micro | ocontroller. | | |
| 14. | Write the use of 825 | 1 chip. | | | |
| 15. | What is meant by cle | osed loop control? | | | |
| | | PART - C (5 x 1 | 6 = 80 Marks) | | |
| 16. | (a) Draw the hardw functions of each | are architecture of 8085 h block. | microprocessor and | explain the (16) | |
| | | Oı | • | | |

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| | (b) | Draw the timing diagram for opcode fetch and memory write machine cycle at explain its operations. (10) | |
|-----|-----|---|------------|
| 17. | (a) | Define instruction. Explain the types of instructions in an Intel 8086 Microprocess with example. (1 | sor (6) |
| | | Or | |
| | (b) | Write an Intel 8085 Assembly language program to add two 16 bit numbers by usin DAD instruction. (1 | ng (6) |
| 18. | (a) | Explain the five types of addressing modes supported by 8085 instruction set with necessary examples. | ith (6) |
| | | Or | |
| | (b) | Draw the architecture of 8051 microcontroller and explain the functions of ea block (1 | ch (6) |
| 19. | (a) | Design a microprocessor based system for the Intel 8085 microprocessor such that should contain 8 K of EPROM using 2 K EPROM IC, 4K of RAM using 2K RA and 3 numbers of 8255. | M |
| | | Or | |
| | (b) | With neat sketch explain the operation of INTEL 8253 Timer/Counter. (10 | 6) |
| 20. | (a) | Draw and explain the hardware circuit required for interfacing a washing machine microcontroller. (1 | to 6) |
| | | Or | |
| | (b) | Draw the circuit diagram for stepper motor control using 8051 microcontroller and write an ALP to run the stepper motor using 8051. (1 | 6) |
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