Reg. No.:					

Question Paper Code: 55043

B.E. / B.Tech. DEGREE EXAMINATION, NOV 2017

Fifth Semester

	1	Electronics and Com	munication Engineeri	ng				
1	5UEC503 - MICROP	PROCESSORS, MIC	ROCONTROLLERS	AND APPLICATIONS				
		(Regula	tion 2015)					
Du	ration: Three hours	Answer A	LL Questions	Maximum: 100 Marks				
		PART A - (5	x 1 = 5 Marks					
1.	What is the Maximu	m clock frequency ir	ı 8086?					
	(a) 5 MHz	(b) 5 Hz	(c) 20 MHz	(d) 15 MHz				
2.	In 8279, a scanned goes to interr		e, if a sensor changes	s its state, the line				
	(a) CS, high	(b) A0, high	(c) IRQ, high	(d) STB, high				
3.	3. The 8051 can handle interrupt sources.							
	(a) 3	(b) 4	(c) 5	(d) 6				
4.	In reading the columnotation.	mns of a matrix, if	no key is pressed we	e should get all in binary				
	(a) 0	(b) 1	(c) F	(d) 7				
5.	5. Which of the following is not a single bit instruction in AVR?							
	(a) SBI	(b) PORT	(c) CBI	(d) All the above				
		PART - B (5	x 3 = 15 Marks)					
6.	Differentiate Proced	ure and Macro direct	ives in 8086.					

7. Categorize the basic modes of operation of 8255.

8.	If a 12 MHz crystal is connected with 8051, how much is the time taken for the count in timer 0 to get incremented by one?
9.	What are the applications of microcontroller?

PART - C (5 x 16 = 80 Marks)

10. What are the classifications of AVR microcontrollers?

- 11. (a) (i) Describe the internal architecture of 8086 Microprocessor with neat diagrams. (10)
 - (ii) Analyzing an 8086 ALP for Multiplication of two 8-bit numbers. (6)

Or

(b) (i) Illustrate the classification of 8086 instructions based on its word length and Explain the data transfer, arithmetic and branch instructions with examples.

(10)

- (ii) Demonstrate about the assembler directives with an example for SEGMENT, TYPE and OFFSET in 8086. (6)
- 12. (a) (i) How to interface a DMA controller with a microprocessor? Assess how DMA controller transfers large amount of data from one memory locations to another memory location. (8)
 - (ii) Discuss how an 8259 is interfaced to an 8086 based system. How does 8259 service an interrupt. (8)

Or

- (b) (i) How do you interface a keyboard and the display using keyboard/display controller? (8)
 - (ii) Draw and explain the block diagram of 8254 Programmable Interval Timer and also explain the various modes of operation. (8)
- 13. (a) (i) Explain the different addressing modes in 8051 microcontroller and discuss each mode with an example. (8)
 - (ii) Mention the I/O pins, ports and circuits of 8051 microcontroller. (8)

Or

(b) Explain in detail about the architecture of 8051 microcontroller with neat diagram.

(16)

14.	(a)	Draw the diagram to interface a stepper motor with 8051 microcontroller are explain. Write its ALP to run the stepper motor in both forward and reverse direction with delay.	on
		Or	
	(b)	(i) With a neat circuit diagram, explain how 4x4 Keypad is in interfaced with 805 microcontroller.	51 8)
		(ii) Analyzing how to interface a 16 X 2 LCD display using 8051 microcontroller.	8)
15.	(a)	Elaborate in detail about the architecture of AVR microcontroller with its register (10	
		Or	
	(b)	(i) Explain the various addressing modes of AVR in detail. (8	3)
		(ii) Describe in detail about the instruction set of AVR controller. (8)	8)