Reg. No.:					

## **Question Paper Code: 53115**

## B.E. / B.Tech. DEGREE EXAMINATION, NOV 2017

## Third Semester

	Biomedical E	ngineering					
	15UBM305 - SEMICONDUCTO	R DEVICES AN	ND CIRCUITS				
	(Regulation	on 2015)					
Du	ration: Three hours  Answer ALL	Questions	Maximum: 100 Marks				
	PART A - (10 x	1 = 10 Marks)					
1.	. At room temperature the current in an intrinsic semiconductor is due to						
	(a) holes (b) electrons	(c) ions	(d) Holes and Electrons				
2.	The most commonly used semiconductor m	aterial is					
	<ul><li>(a) silicon</li><li>(c) mixture of silicon and germanium</li></ul>	<ul><li>(b) germanium</li><li>(d) none of these</li></ul>					
3.	For an n-channel enhancement type MOSFET, if the source is connected at a hard potential than that of the bulk ( $V_{SB} > 0$ ), the threshold voltage $V_T$ of the MOSFET was						
	<ul><li>(a) remain unchanged</li><li>(c) change Polarity</li></ul>	<ul><li>(b) decrease</li><li>(d) increase</li></ul>					
4.	Which of the following is used for generating	ng time varying v	vave forms?				
	<ul><li>(a) MOSFET</li><li>(c) Tunnel diode</li></ul>	(b) PIN diode (d) UJT					
5.	Pulse stretching, time-delay, and pulse gene type of multivibrator circuit?	ration are all eas	ily accomplished with which				
	<ul><li>(a) astable</li><li>(c) multistable</li></ul>	<ul><li>(b) monostable</li><li>(d) bistable</li></ul>					
6.	An astable multivibrator requires						
	(a) halanced time constants	(b) a pair of m	atched transistors				

(d) dual J-K flip-flops

(c) no input signal

7.	Only the condition = _	must be satisfied for self-sustained oscillations to result.						
	(a) 0	(b) -1	(c) 1	(d) none of these				
8.	The feedback signal in divider in the LC circu	ne feedback signal in a(n) oscillator is derived from an inductive voltage vider in the LC circuit.						
	(a) Hartley	(b) Armstrong	(c) Colpitts	(d) RC Phase shift				
9.		amplifiers are normally operated in a push-pull configuration in order to e an output that is a replica of the input.						
	(a) A	(b) B	(c) C	(d) AB				
10.	That is the maximum efficiency of a class A circuit with a direct or series-fed onnection?							
	(a) 90%	(b) 78.5%	(c) 50%	(d) 25%				
		PART - B (5 x	2 = 10  Marks					
11.	Distinguish between av	valanche break dow	n and Zener breal	k down.				
12.	. What does UJT stand for? Justify the name UJT.							
13.	. Mention the use of Schmitt trigger circuit.							
14.	List the advantages of	negative feedback a	mplifiers.					
15.	Draw the hybrid small parameters.	signal model of co	ommon base conf	iguration. Define fou	r hybrid			
		PART - C (5 x	16 = 80 Marks)					
16.	(a) Explain the open characteristics.	ation of common	emitter connec	etion of transistor v	with its (16)			
		C	)r					
	(b) Explain the working	ng of Zener diode ar	nd draw its V-I ch	aracteristics.	(16)			
17.	(a) With the help of su	iitable diagram expl	ain working of D	epletion MOSFET.	(16)			
		C	)r					
	(b) Describe the operation.	ation of UJT as a re	elaxation oscillato	or and derive its frequ	iency of (16)			
18.	(a) Explain the opera		principle of mo	nostable multivibrate	ors with (16)			
			<b>3</b>					

- (b) Explain the operation of a full wave rectifier circuit with various parameters that govern its performance. (16)
- 19. (a) Explain in detail the Voltage series feedback connection and Current shunt feedback connection of amplifiers. (16)

Or

- (b) Explain with neat diagram, the working of Hartley oscillator using transistor. Derive an expression for frequency of oscillation. (16)
- 20. (a) Derive the expression for current gain, input impedance and voltage gain of a CE transistor amplifier. (16)

Or

(b) Explain the operation of a class B amplifier with neat diagram. Derive the expression for its maximum efficiency. Mention its drawback and the methods to overcome it. (16)