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Question Paper Code: 45804

B.E. / B.Tech. DEGREE EXAMINATION, MAY 2018

Fifth Semester

Information Technology

14UIT504 - EMBEDDED COMPUTING SYSTEMS

(Regulation 2014)

Duration: Three hours

Maximum: 100 Marks

Answer ALL Questions

PART A - (10 x 1 = 10 Marks)

- Which of the following are the two main components of the CPU.
 - Control Unit and Registers
 - Registers and Main Memory
 - Control unit and ALU
 - ALU and bus
- An embedded system must have
 - Hard disk
 - Processor and memory
 - Operating system
 - Processor and input-output units
- Bus is a set of
 - Wires
 - Cards
 - Chips
 - Pins
- _____ is the hardware signal to be set when the timer reaches zero.
 - I/O Flag
 - Zero Flag
 - Status Flag
 - Reset Flag
- _____ is defined as a process of selection which says that a process has right to use the processor at given time.
 - Program
 - Scheduling
 - Relocation
 - Controller

6. In real time operating system
- (a) Control flow graphs (b) Control/data flow graphs
(c) Data flow graph (d) Flow graph
7. Which process can be affected by other processes executing in the system?
- (a) cooperating process (b) child process
(c) parent process (d) init process
8. Which one of the following is not a real time operating system?
- (a) POSIX (b) Windows CE
(c) RTLinux (d) Palm OS
9. _____ is used to compress video.
- (a) JPEG (b) JPG (c) MPEG (d) PNG
10. In avionics architecture _____ units can be easily plugged and unplugged into aircraft during maintenance.
- (a) SRU (b) LRU (c) SAFE (d) CPMs

PART - B (5 x 2 = 10 Marks)

11. Name some of the hardware parts of embedded systems?
12. Name any two techniques used to optimize execution time of program?
13. Mention the different styles of IPC?
14. Mention the networks for distributed embedded systems.
15. What is prototype?

PART - C (5 x 16 = 80 Marks)

16. (a) (i) Explain the various data operations involved in ARM. (8)
(ii) Implement an if statement in ARM. (8)

Or

- (b) What do you mean by throwing an exception? How is the exception condition during execution of a function handled. (16)
17. (a) With a neat sketch, explain the role of assemblers and linkers in compilation process. (16)

Or

(b) Explain in detail about program validation and testing. (16)

18. (a) Explain Inter-process communication and synchronization with signals. (16)

Or

(b) Explain in detail about inter process communication mechanisms. (16)

19. (a) Discuss about different types of design flows used for design methodologies. (16)

Or

(b) Explain in detail about quality assurance. (16)

20. (a) Summarize the sequence diagram of taking picture with digital still camera. (16)

Or

(b) Draw and discuss the block diagram and architecture of Video accelerator. (16)
