Reg. No. :

Question Paper Code: 37404

B.E. / B.Tech. DEGREE EXAMINATION, MAY 2018

Seventh Semester

Electronics and Communication Engineering

01UEC704 - EMBEDDED AND REAL TIME SYSTEMS

(Regulation 2013)

Duration: Three hours   Maximum: 100 Marks

Answer ALL Questions

PART A - (10 x 2 = 20 Marks)

1. Differentiate Von Neumann and Harvard architecture.

2. List out the two power management features provided by CPUs.

3. What is BIOS?

4. What does a linker do?

5. State the functions of operating system.

6. Define context switching.

7. What is best effort routing?

8. What is the use of attached accelerator to CPU?

9. State the function of Set-Top-Box.

10. List out the advantages of FOSS.

PART - B (5 x 16 = 80 Marks)

11. (a) (i) Discuss the two basic data types supported by ARM architecture.   (8)

    (ii) Explain the ARM programming model.   (8)

Or

(b) Explain briefly the model train controller system. (16)

12. (a) Explain on how on chip memory management schemes can improve higher speed process. (16)

Or

(b) Why not use the source code directly? Explain briefly the fundamental model for programs. (16)

13. (a) Describe in detail about the inter process communication mechanism. (16)

Or

(b) Why need multiprocessors? Analyze the performance of the system with multiple processors. (16)

14. (a) Demonstrate the operation of Internet enabled system. With a suitable example. (16)

Or

(b) Briefly describe the design of accelerators with an algorithm. (16)

15. (a) Explain the design procedure of data compressor with its specifications and requirements. (16)

Or

(b) Discuss the design of personal digital assistants with step by step procedure. (16)

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