Reg. No. :

Question Paper Code: 44022

B.E. / B.Tech. DEGREE EXAMINATION, NOV 2017

Fourth Semester

Computer Science and Engineering

14UCS402 - SOFTWARE TESTING

(Regulation 2014)

Duration: Three hours   Maximum: 100 Marks

Answer ALL Questions

PART A - (10 x 1 = 10 Marks)

1. CMM model refers to

   (a) Computer Maturity Model   (b) Capability Maturity Model
   (c) Capability Multi Model   (d) Computer Multi Model

2. Test Oracle in software testing is used to

   (a) Determine whether test result is pass or fail
   (b) Fix the result as pass
   (c) Fix the result as optimum
   (d) Masking the changes in the result

3. _____ testing comes under black box testing

   (a) Unit testing   (b) Functional testing
   (c) System testing   (d) Both b and c

4. Decision table is a

   (a) Data structure   (b) Chart illustrating cause and effect of failure
   (c) Tool for error analysis   (d) Testing package

5. The auxiliary code developed to support testing of units and components is called

    (a) Unit   (b) System   (c) Integration   (d) Test harness

6. Which one of the following is not a type of system test?

    (a) Security test   (b) Recovery test
    (c) Reliability test   (d) Availability test

7. The Test incident report is also called as

    (a) Test report   (b) Problem report
    (c) Document report   (d) Validity report

8. The basic test plan components as described in _____ standard.

    (a) IEEE 829   (b) IEEE 1061   (c) IEEE 12007   (d) IEEE 730

9. The _____ plans for inspection, sets the date, invites the participants, distributes the required documents, runs the inspection meeting, appoints a recorder to record results and monitors the follow up period after the review.

    (a) Requirement Analyst   (b) Requirement Analyst
    (c) Developer   (d) Inspection Leader

10. _____ consists of developing and applying a set of corrective actions to get a project on track when monitoring shows a deviation from what was planned.

    (a) Project monitoring   (b) Project controlling
    (c) Both a and b   (d) None of the above

PART - B (5 x 2 = 10 Marks)

11. Differentiate verification and validation.

12. What is meant by STG chart?

13. How the acceptance test cases are prepared?

14. Enumerate the test plan attachments?

15. What does fault seeding ratio mean?

PART - C (5 x 16 = 80 Marks)

16. (a) Discuss the defect classes, the defect repository and test design with an defect example of the Coin problem. (16)

Or

(b) Explain the difficulties and challenges are faced by the software tester. (16)

17. (a) Briefly explain about loop and mutation testing. (16)

Or

(b) State the implications of positive and negative testing ? What are their requirements? Illustrate when do you perform boundary value analysis? Explain the status with an example. (16)

18. (a) A tester wants to test the online retail application system as whole module or entire application, explain in detail what are the different types of testing to be tested in this application? (16)

Or

(b) Explain about the security testing in software testing. (16)

19. (a) Summarize the test plan components. (16)

Or

(b) What are the steps for test planning? Explain the test plan components and attachments? How this is categorized in test management. Analyze. (16)

20. (a) Write a detailed note on the components of review plans. (16)

Or

(b) Explain in detail about the various elements to be checked at design review. (16)

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