Reg. No.:					

Question Paper Code: 52153

M.E. DEGREE EXAMINATION, DECEMBER 2015

First Semester								
	Power Electronics and Drives							
	15PPE103 - MODERN POWER SEMI CONDUCTOR DEVICES							
	(Regulation 2015)							
	Duration: Three hours			Maximum: 100 Marks				
	Answer ALL Questions							
	PART A - $(10 \times 1 = 10 \text{ Marks})$							
1.	. Reverse recovery current in a diode depends upon							
	(a) PIV		(b) Temperature					
	(c) Storage charge		(d) Forward field current					
2.	2. The reverse recovery time of diode is $t_{IT} = 3\mu s$ and the rate off all of the diode current in di/dt = 30 $A/\mu s$. The storage charge current Q_{RR} is							
	(a) 130 μs	(b) 140 <i>μs</i>	(c) 135 μs	(d) 145 μs				
3.	The number of PN junct	tions in a SCR is						
	(a) 1	(b) 2	(c) 3	(d) 4				
4.	The SCR can be termed	as						
(a) DC switch		(b) AC switch						
	(c) Square-wave sw	itch	(d) Either a or b					
5.	Which one is most su application?	itable power devic	e for high frequen	cy (>100kHZ) switching				
	(a) BJT		(b) Power MOSFE	Т				
	(c) Schottky diode		(d) Microwave tran	sistor				

6.	The controlling pa	rameter in MOSFE	Γis		
	(a) V_{ds}	(b) I _g	(c) V _{gs}	(d) I _s	
7.	Opto couplers cor	nbine			
	(a) IGBTs and	MOSFETs			
	(b) Power tran	sistor and silicon tra	nnsistor		
	(c) SITs and B	JTs			
	(d) Infrared lig	th emitting diode an	nd a silicon photo trai	nsistor	
8.	Snubber circuit is	used to limit the rate	e of		
	(a) Rise of cur	rent	(b) Conduction	n period	
	(c) Rise of vol	tage across SCR	(d) none of th	e above	
9.	Thermal resistance	e in SCRs has the un	its of		
	(a) ${}^{0}C/W$ and 1	neat sinks (HS) are r	nade from aluminum		
	(b) $W/^{0}C$ and	HS are made from s	teel		
	(c) ${}^{o}C/W$ and	HS are made from	copper		
	(d) ${}^{o}C/W$ and	HS are made from	copper alloy		
10.				s its heat sink at 90°C unction temperature v	
	(a) $63^0 C$	(b) 107 0 C	(c) $117^0 C$	(d) $127^0 C$	
		PART - E	$3 (5 \times 2 = 10 \text{ Marks})$		
11.	Distinguish between	en power diode and	linear diode.		
12.	What are the differ	rences between conv	verter grade and inver	ter grade thyristors?	
13.	Compare RCT and	FCT.			
14.	State the necessity	of isolation.			
15.	Write the importan	nt guideline for heat	sink design.		
		PART - C	$(5 \times 16 = 80 \text{ Marks})$		

16. (a) (i) Discuss the device selection strategies of power semiconductor devices in detail.

(8)

		(ii)	Elucidate in detail the EMI impact due to switching of the power semiconductives.	ctor (8)				
	Or							
	(b)	(i)	Discuss the on-state and switching losses of power devices.	(8)				
		(ii)	Draw the symbols of four power switching devices and mention few application of each device.	ions (8)				
17.	(a)	(i)	How BJT and thyristor act as current controlled devices? Prove this concept its switching characteristics.	t by (8)				
		(ii)	With neat sketch, explain the Darlington pair of thyristors and its characterist	tics. (8)				
	Or							
	(b)	(i)	Discuss the need of connecting devices in series/parallel. Also explain drawback of the same.	the (8)				
		(ii)	Obtain the steady state and dynamic model of BJT.	(8)				
18.	(a)	Exp	plain the features of the following ((16)				
		(i) GTO						
		(ii)	MCT					
		(iii)	FCT					
		(iv)	RCT					
			Or					
	(b)	(i)	Draw and explain the static and switching characteristics of power MOSFET.	(8)				
		(ii)	Describe the basic structure of IGBT.	(8)				
19.	(a)	(i)	Explain over voltage and over current protection of SCR.	(8)				
		(ii)	Draw and explain the base drive circuit of power BJT.	(8)				
	Or							
	(b)	(i)	Write the importance of pulse transformer and Opto coupler.	(8)				
		(ii)	Write short notes about Snubber circuits.	(8)				

20. (a) Explain the transient thermal impedance for power semiconductor devices.

Or

(b) (i) Describe the method of designing the heat sinks for thyristors.

(ii) Write short notes on liquid cooling and vapour phase cooling.

(8)