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## B.E. / B.Tech. DEGREE EXAMINATION, NOV 2018

Fourth Semester

Electrical and Electronics Engineering

## 14UEE404 - ANALOG INTEGRATED CIRCUITS

(Common to Instrumentation and Control Engineering)

(Regulation 2014)

Duration: Three hours Maximum: 100 Marks

**Answer ALL Questions** 

PART A -  $(10 \times 1 = 10 \text{ Marks})$ 

- 1. In Monolithic IC
  - (a) Performance depends on the substrate
  - (b) Performance does not depend on the substrate
  - (c) Performance depends on interconnects
  - (d) Performance depends on packaging

2.	How many	leads doe	es the TO-5 n	netal can	package of an	operational	amplifier	have
	( ) 0 10	4.0	a > - 0	1.0	( ) 0	4.4	(1) 0	

- (a) 8, 10, or 12
- (b) 6, 8, or 10
- (c) 8 or 14
- (d) 8 or 16
- 3. If the currents flowing towards the inverting and non-inverting terminals of an Op-amp are 3  $\mu$ A and 1  $\mu$ A respectively, the bias current and the input offset current are
  - (a) 3  $\mu A$  and 1  $\mu A$

(b)  $2 \mu A$  and  $2 \mu A$ 

(c)  $3 \mu A$  and  $2 \mu A$ 

- (d) 1  $\mu$ A and 2  $\mu$ A
- 4. What is the scale multiplier (factor) of a basic integrator?
  - (a) R/C
- (b) C/R

- (c) -RC
- (d) 1/RC

5.	An instrumentation amplifier uses								
	(a) 1 op-amp	(b) 2 op-amps	(c) 4	l op-amps	(d) 3 op-am	ps			
6.	In applications where circuit recommended is	measurement of a j	physical	quantity is in	volved, the C	)p-amp			
	<ul><li>(a) Basic non-inver</li><li>(c) An active filter</li></ul>	<ul><li>(b) A comparator</li><li>(d) An instrumentation amplifier</li></ul>							
7.	7. A 555 timer in monostable application mode can be used for								
	(a) Pulse position modulation			(b) Frequency shift keying					
	(c) Speed control and measurement			(d) Digital phase detector					
8.	How many V <sub>cc</sub> connects	ions does the 565 PLI	L use?						
	(a) 0 (b)	1	(c) 2	(0	1) 3				
9.	Voltage regulator LM78	805 has an output vol	tage of						
	(a) 5 volts	(b) -5 volts	(c) 0.5 v	olts (d	l) -0.5 volts				
10.	What is the range of the	voltage level of the	LM 723 a	djusted voltag	e regulator?				
	(a) 0 V to 5 V	(b) 1.2 V to 37	V (c) -	5 V to -24 V	(d) 5 V to 2	4 <i>V</i>			
	PART - B (5 x $2 = 10 \text{ Marks}$ )								
11.	11. Why aluminum is preferred for metallization?								
12.	12. Generally, IC 741 is not used for high frequency applications. Justify.								
13.	13. List the applications of Log amplifier.								
14.	14. Give the applications of multiplier IC.								
15.	Give the drawbacks of l	inear regulators.							
		PART - C (5 x 16	6 = 80  Ma	rks)					
16.	(a) Mention the dimer packaging of IC's	nsions of a typical List the steps invo	_			_			

with necessary diagram wherever necessary.

Integrated Circuits. Discuss the following processes in the monolithic IC technology

	(i) Epitaxial Growth, (ii) Isolation by diffusion. (16	j)
	Or	
b)	Briefly explain the various processes involved in the fabrication of monolith bipolar transistor.	
17. (a)	With the necessary diagrams, describe the working principles of different external frequency compensation methods. (16	5)
	Or	
(b)	Explain the operation of the following applications of op-amp, also derive a expression for the output (a) differentiator (b) Integrator. (16)	
18. (a)	(i) What is an Instrumentation Amplifier? Draw a system whose gain is controlled by an adjustable resistance. Name the circuit that is used to detect the peak value of the non-sinusoidal waveforms.	
	(ii) Draw the circuit of a voltage to current converter if the load is floating are grounded. Is there any limitation on the size of the load when grounded Discuss.	
	Or	
(b)	(i) Sketch the circuit diagram of clamper and explain its operation. (8	3)
	(ii) Explain the operation of successive approximation A/D converter. (8	3)
19. (a)	Describe the working principle voltage controlled oscillator and derive the equation (10)	
	Or	
(b)	Perform the closed-loop analysis of Phase Locked Loop (PLL) and derive the transfer function of PLL. Also derive the expressions for the lock-in and capturing range of IC 565 Phase Locked Loop (PLL) with neat diagram. Calculate the output frequency, lock-range and capture-range of IC 565. Assume R1=10 $\Omega$ , $C_1=0.01~\mu$ and $C=20~\mu$ F.	re ut ıF

20. (a) With neat circuit diagram explain the working of IC 8038 function generator. (16)

Or

- (b) (i) Mention the different configurations of a switching regulator. Draw any one basic block diagram and discuss its operation in detail. (8)
  - (ii) With a neat schematic diagram, describe a monolithic IC Audio power amplifier (LM 380). State its advantages over conventional power amplifiers. (8)