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| Reg. No.: |
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## **Question Paper Code: 55303**

## B.E./B.Tech. DEGREE EXAMINATION, NOV 2018

## Fifth Semester

## Electrical and Electronics Engineering

| 15UEE503- Microprocessors and Microcontroller Programming |   |                         |  |              |         |  |
|---|---|-------------------------|--|--------------|---------|--|
| (Regulation 2015)   |   |                         |  |              |         |  |
| Dura  | ation: Three hours  |                         | N  | Maximum: 100 | ) Marks |  |
|   |   | Answer Al               | l Questions  |              |         |  |
|   | PART A - $(10 \times 1 = 10 \text{ Marks})$                       |                         |  |              |         |  |
| 1.  | 1. When an 8085 microprocessor is reset, the address bus contains |                         |  |              | CO1-R   |  |
|   | (a) 0000H   | (b) 0002H               | (c) 0043H  | (d) 003C H   |         |  |
| 2.  | The 8085 microproce<br>T-state value is                           | essor uses a crystal of | frequency 6.25 Mhz. The                                  |              | CO1-R   |  |
|   | (a) 320 ns  | (b) 640 ns              | (c) 960  | (d) 1280 ns  |         |  |
| 3.  | 6.5, RST 5.5) by se   | L .                     | interrupts (RST 7.5, RST form masks or generate D) line. |              | CO2-R   |  |
|   | (a) SIM   | (b) RIM                 | (c) EI   | (d) DI       |         |  |
| 4.  | Call instruction is a   | instruction             |  |              | CO2-R   |  |
|   | (a) 3 byte  | (b) 2 byte              | (c) 4 byte   | (d)1 byte    |         |  |
| 5.  | 5. The registers that contain the status information is           |                         |  |              | CO3-R   |  |
|   | (a) Control registers   |                         | (b) Status registers                                     |              |         |  |
|   | (c) Program status word   |                         | (d) All of the mentioned                                 |              |         |  |
| 6.  | The device that gener circuit using crystal of                    |                         | clock signal for the operation                           | on of the    | CO3-R   |  |
|   | (a) Timing unit   |                         | (b) Timing and control un                                | it           |         |  |
|   | (c) Oscillator  |                         | (d) Clock generator                                      |              |         |  |
|   |   |                         |  |              |         |  |

| 7.  | In direct memory access mode, the data transfer takes place                   |  |  |  | CO4-R      |           |
|-----|---|--|--|--|------------|-----------|
|     | (a) Directly  |  | (b) Indirectly                             |  |            |           |
|     | (c) l   | Directly and indire                      | ectly                                      | (d) None of the above                  |            |           |
| 8.  |   | flag that increr<br>ration to the displa | •  | after each read or write               |            | CO4-R     |
|     | (a) l   | F  | (b) RF                                     | (c) AI                                 | (d) WD     |           |
| 9.  |   | ong the four grou                        | =  | anks, the number of groups             |            | CO5-R     |
|     | (a) 1   | 1  | (b) 2                                      | (c) 3                                  | (d) all of | the four  |
| 10. | The   | operations perfor                        | med by data transfer                       | instructions are on                    |            | CO5-R     |
|     | (a) t   | oit data                                 | (b) byte data                              | (c) 16 bit data                        | (d) all of | the above |
|     |   |  | PART - B (5                                | x 2= 10 Marks)                         |            |           |
| 11. | List  | out the various st                       | teps involved to fetch                     | a byte in 8085?                        |            | CO1- R    |
| 12. | Explain the difference between a JMP instruction and CALL instruction. CO2- R |  |  |  |            |           |
| 13. | . List the features of 8051 microcontroller.                                  |  |  |  |            | CO3- R    |
| 14. | What is an USART?   |  |  |  |            | CO4- R    |
| 15. | Wri   | te a program to fi                       | nd 2's complement u                        | sing 8051.                             |            | CO5- R    |
|     |   |  | PART – C                                   | (5 x 16= 80Marks)                      |            |           |
| 16. | (a)   | Explain the arch block diagram.          | nitecture of 8085 mic                      | roprocessor with functional            | CO1-U      | (16)      |
|     |   |  | Or   |  |            |           |
|     | (b)   | Sketch the Tim instruction.              | ing diagram of 808                         | 5processor for MOV A,B                 | CO1-U      | (16)      |
| 17. | (a)   |  | ple explain data transtructions of 8085 mi | ansfer and logical & bit croprocessor. | CO2-Ana    | (16)      |
|     | (b)   | hexadecimal nu                           | _  | language program to add                | CO2- C     | (16)      |

| 18. | (a) | Explain in detail about the memory organization of 8051 microcontroller.   | CO3- Ana | (16) |
|-----|-----|--|----------|------|
|     |     | Or   |          |      |
|     | (b) | Explain the interrupt structure of 8051 microcontroller and also explain how interrupts are prioritized.   | CO3- Ana | (16) |
| 19. | (a) | (i) With a neat block diagram explain the function of 8255 PPI .   | CO4-U    | (12) |
|     |     | (ii) Show the control word format of 8255 and explain how each bit is programmed.  | CO4-U    | (4)  |
|     |     | Or   |          |      |
|     | (b) | Explain how to interface Digital to Analog converter with 8085 microprocessor and write an assembly programme to convert the given digital data into its equivalent analog data. | CO4-Ana  | (16) |
| 20. | (a) | Draw the schematic for interfacing a stepper motor with 8051 microcontroller and write 8051 ALP for changing speed and direction of motor.                                       | CO5-U    | (16) |
|     |     | Or   |          |      |
|     | (b) | With a neat circuit diagram explain how a 4 x 4 keypad and seven segment display is interfaced with 8051 microcontroller.  | CO5-U    | (16) |