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Question Paper Code: 31744

B.E. / B.Tech. DEGREE EXAMINATION, NOV 2016

Seventh Semester

Electronics and Communication Engineering

01UEC704 - EMBEDDED AND REAL TIME SYSTEMS

(Regulation 2013)

Duration: Three hours Maximum: 100 Marks

Answer ALL Questions

PART A - $(10 \times 2 = 20 \text{ Marks})$

- 1. List the functions of ARM processor in supervisor mode.
- 2. How is ARM processor different from other processors?
- 3. What is a data flow graph?
- 4. What does a linker do?
- 5. Define context switching.
- 6. What are the three conditions that must be satisfied by the re-entrant function?
- 7. What is the use of attached accelerator to CPU?
- 8. Differentiate counter semaphore and binary semaphores.
- 9. What are FOSS tools for embedded systems?
- 10. Give the steps to destroy a message queue.

PART - B (5 x
$$16 = 80 \text{ Marks}$$
)

11. (a) (i) What are the parameters to be considered while designing an embedded system process? (8)

(8)

(ii) Explain about cache memory in ARM processor.

	(b)	(i) An	alyze the concept of pipelining.	(8)
		(ii) De	termine various instruction set preliminaries of ARM processor.	(8)
12. ((a)	Explair process	n on how on chip memory management schemes can improve higher s	peed (16)
			Or	
	(b)	(i) Lis	et the various compiler optimization techniques.	(8)
		(ii) Ho	w do you test the microcontroller based electronic voting machine.	(8)
13.	(a)	(i) De	scribe process scheduling in detail.	(8)
			umerate the context switch mechanism for moving the CPU from ecuting process to another.	one (8)
			Or	
	(b)	Describ	be in detail about the inter process communication mechanism.	(16)
14.	(a)		strate about the cache problem in a system involving an accelerator gest a method to overcome it.	and (8)
		(ii) Dis	scuss in detail about I ² C bus.	(8)
			Or	
	(b)	Demon	strate the operation of Internet enabled system. With a suitable example.	(16)
15.	(a)	Discuss	s about the design of data compressor in detail.	(16)
			Or	
	(b)	Write s	horts notes on the following:	
		(i) FO	SS tools for embedded system development.	(8)
		(ii) Per	rsonal digital assistant.	(8)