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Question Paper Code: 41427

B.E. / B.Tech. DEGREE EXAMINATION, NOV 2016

Second Semester

Electronics and Communication Engineering

14UEC207 - ELECTRONIC DEVICES

(Regulation 2014)

Duration: Three hours

Maximum: 100 Marks

Answer ALL Questions

PART A - (10 x 1 = 10 Marks)

- The forbidden energy gap for Si is
(a) 1.1 eV (b) 1.5 eV (c) 1.7 eV (d) 0.92 eV
- Einstein relationship for semiconductor is
(a) $T/11,600$ (b) $T^2/11,600$ (c) $T/11,000$ (d) None of these
- The field across the depletion region becomes very high of the order of
(a) 4×10^7 V/m (b) 2×10^7 V/m (c) 4×10^9 V/m (d) 3×10^7 V/m
- Reverse resistance in the range of
(a) $M\Omega$ (b) $m\Omega$ (c) $K\Omega$ (d) None of these
- The another name of universal bias circuit is
(a) Collector to base bias circuit (b) Emitter bias circuit
(c) Voltage divider bias (d) Fixed bias with emitter
- Output resistance of the transistor amplifier is highest in
(a) CB (b) CE (c) CC (d) None of these

Or

(b) Explain the Zener diode and its characteristics. (16)

18. (a) Describe the following configuration and its characteristics (i) Common base configuration (ii) Common emitter configuration. (16)

Or

(b) Explain about switch mode power supply and its operation. (16)

19. (a) With the help of suitable diagrams explain the working of different types of MOSFET. (16)

Or

(b) Explain the construction and operation of N-channel JFET. (16)

20. (a) Explain the operation and characteristics of SCR. (16)

Or

(b) Write short notes on

(i) Phototransistor

(ii) Photodiode

(iii) Photoconductive sensor

(iv) Photovoltaic sensors (16)
