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Question Paper Code: 52306

B.E. / B.Tech. DEGREE EXAMINATION, DEC 2020

Second Semester

Civil Engineering

01UEE206- BASIC ELECTRICAL AND ELECTRONICS ENGINEERING

(Common to Mechanical Engineering)

(Regulation 2013)

	Duration: 1.15 hrs	Maximum: 30 Mar						
	PART A - (6 x 1	= 6 Marks)						
	(Answer any six of the f	following ques	stions)					
1.	 If 750 μA is flowing through 11 kΩ of resistaresistor? 	750 μ A is flowing through 11 k Ω of resistance, what is the voltage drop across t sistor?						
	(a) 8.25 V (b) 82.5 V (c) 14.6	5 V (d) 146 V					
2.	Which of the following are integrating instruments?							
	(a) Ammeters	(b) Voltmeters						
	(c) Wattmeters	(d) Ampere-hour and watt-hour meters						
3.	3. A transformer							
	(a) changesAC to DC	(b) changes DC to AC						
	(c) steps up or down DCvoltages	(d) steps u	(d) steps up or down ACvoltages					
4.	4. A D.C. generator works on the principle of							
	(a) Lenz's law (b) Ohm's law (c) F	araday's law	(d) None of the above					
5.	5. The barrier potential for a silicon diode at 25°	C is approxim	ately					
	(a) $0.4V$ (b) $0.3V$	(c) 0.7V	(d) 0.5V					
6.	6. When both emitter and collector junctions a which region?	are forward bi	iased, the transistor is in					

(b) Cut-off

(a) Active

(c) Break down

(d) Saturation

7.	Convert (1111 (a) 267	0111) ₂ to Octal (b) 367	(c) 376	(d) 276			
8.	With OR operat	ion. 1+1 is					
	(a) 1	(b) 0	(c) 10	(d) 2			
9.	9. In transistor radio receivers the number of IF amplifier stages are						
	(a) 1	(b) 2	(c) 4	(d) 6			
10.	Radio broadcas	ting is a familiar e	xample of				
	(a) space multiplexing (b) time multiplexing						
	(c) frequen	cy multiplexing	(d) none of	(d) none of the above			
PART – B (3 x 8= 24 Marks)							
(Answer any three of the following questions)							
11.	Briefly expinstrument.	olain the construc	tion and working of attra	action type moving iron (8)			
12.	Explain the principle, construction and working of a D.C motor. Also explain its types. (8)						
13.	Explain the forms.	half wave and ful	ll wave rectifier with neat of	circuit diagram and wave (8)			
14.	Explain wit	h neat sketches the	e output waveform of 4 bit	synchronous counters			
	and draw th	e logic diagram w	ith the help of truth table.	(8)			
15.	Explain the	principle of Amp	litude and Frequency modu	lation. (8)			