						I				
	Qu	estion l	Paper Co	ode: 35304	4					
	B.E. / B.7	Гесh. DEC	GREE EXA	MINATION	I, DEC 20	020				
			Fifth Sem	ester						
	E	Electrical a	and Electron	nics Enginee	ring					
	01UEE504 – MICROPRO	CESSOR	S AND MI	CROCONT	ROLLER	PRO	GRAMMING	G		
		(Regulation	2013)						
D	Duration: One hour				Maxi	mum:	30 Marks			
		PART	A - (6 x 1	= 6 Marks)						
	(Ans	swer any s	six of the fo	ollowing que	estions)					
1.	instruction subroutine sequence	is used	to return	to calling _I	program	after	completing	the		
	(a) RST	(b) C	ALL	(c) RET		(d)	TRAP			
2.	0	The register in the 8085A that is used to keep track of the memory address of the next opcode to be run in the program is the								
	(a) stack pointer			(b) progr	ram coun	ter				
	(c) ALU			(d) accur	mulator					
3.	If 'n' denotes number of the microprocessor is run									

Reg. No.:

4. Direction flag is used with

denoted by

(a) n+T

1.

2.

3.

(a) String instructions

(b) Stack instructions

(d) n/T

(c) n*T

(c) Arithmetic instructions

(b) *n-T*

(d) Branch instructions

5.	For an interrupt to be served by 8051 microcontroller, it should have duration of							
	(a) one machine cycle(c) two machine cycles		(b) three machine cycles(d) four machine cycles					
6.	The instruction that is used to complement the bit of a bit addressable SFR in 8051 microcontroller is							
	(a) CLR C	(b) CPL C	(c) CPL bit	(d) ANL bit				
7.	The register that maintain an original copy of the respective initial current address register and current word register is							
	(a) mode register(c) command register		(b) base address register(d) mask register					
8.	To save the DAC from negative transients the device connected between OUT1 and OUT2 of AD 7523 is							
	(a) p-n junction diode	(b) zener	(c) FET	(d) BJT				
9.	8279 is							
	(a) PPI (c) UART		(b) Keyboard, Display interface(d) USART					
10.	keyboard has 8 interface w	ith 8051 has						
	(a) Return line scan line		(b) Scan line					
	(c) Return line		(d) None of these					
		PART – B (3 x 3	8= 24 Marks)					
	(Answer	any three of the	e following questions)	1				
11.	Build the timing diagram fe	or the instruction	n <i>MVI A</i> , 32.	(8)				
12.	. Develop an assembly language program to calculate the sum of series of even numbers in a given set of array.							
13.	3. Demonstrate the different ways of accessing the internal RAM of 8051.							
14.	4. Explain with neat sketch, the A/D and D/A converter interfacing with 8085 Microprocessor.							
15.	5. Explain about the closed loop control of servo motor using 8051 Microcontrol							

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