		Reg. No. :										
Question Paper Code: 53402												
B.E. / B.Tech. DEGREE EXAMINATION, DEC 2020												
Third Semester												
Electronics and Communication Engineering												
19UEC302 - DIGITAL ELECTRONICS AND DESIGN												
(Regulation 2015)												
Duration: One hour Maximum: 30 Marks												
PART A - (6 x 1 = 6 Marks)												
(Answer any six of the following questions)												
1.	The 2's complement	ent representation of -1	7 is						CO1- U			
	(a) 01110	(b) 01111		(c) 111	10		(d)	10001				
2.	The Boolean function Y=AB+CD is to be realized using only 2-inputCO1-NAND gates. The minimum number of gates required is											
	(a) 2	(b) 3		(c) 4			(d)	5				
3.	How many data se	elect lines are required	for select	ing eig	t inputs	s?			CO2- R			
	(a) 1	(b) 2	(c) 1	3				(d) 4				
4.	The minimum number of 2 to 1 multiplexers requires to realize a 4 to 1 multiplexer is											
	(a) 1	(b) 2	(c) 2	3				(d) 4				
5.	How many natura	l states will there be in	a 4-bit ri	pple co	ounter?				CO3- R			
	(a) 16	(b) 32	(c) -	4				(d) 8				
6.	A shift register that will accept a parallel input or a bidirectional serial CO3- F load and internal shift features is called as?											
	(a) Tristate	(b) End around	(c)	Univer	sal			(d) Co	onversion			
7.		circuits generate the feature on from output of one g	-				e?		CO4- R			
	(a) Synchronous	ynchronous (b) Asynchronous (c) Both a & b (d) None of the above										

8.	Class A circuit is	C	CO4- R							
	(a) Mealy model	(b) Moore model	(c) Both a & b	(d) No	ne of the above					
9.	A register is able	CO5-R								
	(a) Data	(b) Word	(c) Nibble	(d) Bot	(d) Both data and word					
10.	The evolution of I	CO5-R								
	(a) Diode	(b) Resistor	(c) Capacitor		(d) Flip F	Flop				
	$PART - B (3 \times 8 = 24 \text{ Marks})$									
(Answer any three of the following questions)										
11.	Prove the $(A+B)(\overline{A}\ \overline{C}+C)$	following $(\overline{B} + AC) = \overline{AB}$	Boolean	expression	CO1- App	(8)				
12.	Design a 4-bit par logic diagram.	CO2- App	(8)							
13.	Design S-R flip fl	CO3- App	(8)							
14.	Design a sequenti bit manner for any	CO4- Ana	(8)							
15.	Design a Binary-t	CO5- App	(8)							