Reg. No.:					

Question Paper Code: 43502

B.E. / B.Tech. DEGREE EXAMINATION, DEC 2020

Third Semester

Electronics and Instrumentation Engineering

14UEI302 - LINEAR INTEGRATED CIRCUITS AND APPLICATIONS

(Regulation 2014)

Duration: One hour Maximum: 30 Marks

PART A -
$$(6 \times 1 = 6 \text{ Marks})$$

(Answer any six of the following questions)

- 1. An ideal operational amplifier has
 - (a) infinite output impedance

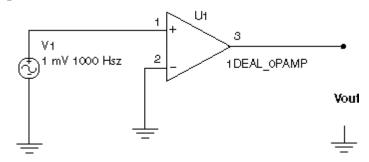
(b) zero input impedance

(c) infinite bandwidth

- (d) all of the above
- 2. Input impedance of an inverting amplifier is approximately equal to
 - (a) Ri

- (b) Rf + Ri
- $(c) \infty$
- (d) Rf Ri

3. Evaluate the output waveform of the circuit?



(a) sine wave

(b) square wave

(c) sawtooth wave

(d) triangle wave

4.	If the gain of a closed-kilo ohms, discriminate		-	t resistor value of	f 1.6					
	(a) 6240 <i>ohm</i> s	(b) 2.4 <i>kilo ohm</i>	s (c) 410 <i>ohms</i>	(d) 0.62 kilo o	hms					
5.	What is the function of	a ladder network?								
		alog signal to a digital ital signal to an analog								
6.	Evaluate the maximum counter in an ADC.	n conversion time of	a clock rate of 1 M	Hz operating a	10-stage					
	(a) 1.024 s	(b) 102.3 <i>ms</i>	(c) 1.024 ms	(d) 10.24	ms					
7.	In a PLL, to obtain lock, the signal frequency must									
8.	(a) come within the loc(b) come within the cap(c) be less than the cap(d) be greater than the An astable multivibrate	oture range ture frequency capture frequency								
	(a) one-shot multiv	ibrator	(b) free-running multivibrator							
	(c) bistable multivib	orator	(d) monostable multivibrator							
9.	What is (are) the princi	pal area(s) of applicat	ion for isolation ampli	fiers?						
	(a) medical	(b) power plant	(c) automation	(d) all of the a	bove					
10.	Which of the following	g circuits is (are) linear	/digital ICs?							
	(a) Comparators(c) Voltage-control	lled oscillators	(b) Timers(d) All of the above							
		PART – B (3 x	8= 24 Marks)							
	(A	nswer any three of th	e following questions	s)						
11.	11. Explain in detail about Silicon wafer preparation and Photolithography.									
12.	2. Explain the frequency compensation techniques of OP-AMP.									

Explain the operation of Schmitt trigger.

13.

(8)

- 14. What is 555 timer? What are the features of 555 timer? Explain the Monostable mode in detail. (8)
- Draw and explain the functional block diagram of a 723 voltage regulator and how this IC can be used as High voltage regulator. (8)