Reg. No. : | | | | | | | | | | |

Question Paper Code: 35502

B.E. / B.Tech. DEGREE EXAMINATION, DEC 2020

Fifth Semester

Electronics and Instrumentation Engineering

01UEI502 – MICROPROCESSOR AND INTERFACING

(Regulation 2013)

Duration: One hour Maximum: 30 Marks

PART A - (6 x 1 = 6 Marks)

(Answer any six of the following questions)

1. The first Microprocessor was

(a) Intel 4004 (b) 8080 (c) 8085 (d) 4008

2. The operating frequency of 8085A microprocessor is

(a) 3MHz (b) 5 MHz (c) 4 MHz (d) 6 MHz

3. Mention data store instruction in a stack memory.

(a) CALL (b) PUSH (c) POP (d) RET

4. Which group of instructions do not affect the flags?

(a) Arithmetic operations (b) Logical operations
(c) Data transfer operations (d) Branch operations

5. Maximum number of interrupts that we can connect with Programmable Interrupt Controller is

(a) 8 (b) 16 (c) 32 (d) 64

6. Mention the type of IC 8253

(a) Programmable interrupt controller (b) Programmable interval timer
(c) Programmable peripheral interface (d) Keyboard display controller

7. How many bits wide is the address bus in 8086 Microprocessor ?s.

(a) 12 bit (b) 10 bit (c) 16 bit (d) 20 bit

8. The no of bits in 8086 flag register is

(a) 8 (b) 10 (c) 13 (d) 16

9. IMUL source is a signed

(a) Multiplication (b) Addition (c) Subtraction (d) Division

10. The IF Flag is called as

(a) Initial Flag (b) Indicate Flag (c) Interrupt Flag (d) Inter Flag

PART – B (3 x 8= 24 Marks)

(Answer any three of the following questions)

11. Explain the operation of the following 8085 signals: Ready, S1 and S0, HOLD and HLDA and ALE. (8)

12. Explain the direct addressing modes and indirect addressing modes of 8085 with example. (8)

13. Relate the detailed concept of interfacing A/D converter with 8085 processor. (8)

14. Classify the various addressing modes of 8086 microprocessor. (8)

15. Develop a program to add two 8 bit data (*F0H* and *50H*) in 8086 processor and store the result in the memory, when MASM assembler is used. (8)