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Question Paper Code: 55301

	B.E	./B.Tech. DEGREE EX	XAMINATION, NOV	2019		
		Fifth S	Semester			
		Electrical and Elec	etronics Engineering			
		15UEE501-POWE	ER ELECTRONICS			
		(Regulat	tion 2015)			
Dur	ation: Three hours	Answer AI	LL Questions	Maximum: 10	0 Marks	
		PART A - (10	x 1 = 10 Marks			
1.	Which semiconduct current triggered dev	or power device out vice?	of the following is	not a	CO1- R	
	(a) Thyristor	(b) G.T.O	(c) Triac	(d) MOS	FET	
2.	The di/dt capacity of	f a thyristor increases			CO1- R	
	(a) When the gate current is zero					
	(b) When the gate current increases					
	(c) When the gate current increases					
	(d) When the anode to cathode voltage rating increases					
3.	The output voltage	waveform of a three Ph	nase square wave inve	rter contains	CO2- R	
	(a) Only even harmo	onics	(b) Both odd and e	ven harmonics		
	(c) Only odd harmon	nics	(d) Only triple harr	monics		
4.	Reduction of harmon	nic content in the outpu	ıt of a system is possil	ole in a inverter	CO2- R	
	(a) Current source	(b) Current source	(c) Square PWM	(d) Load comm	utated	
5.	A freewheeling diod	e is placed across the d	lc load		CO3- R	
	(a) to prevent reversal of load voltage					
	(b) to permit transfer of load current away from the source					

- (c) both (a) and (b)
- (d) None of the above

6.	=		nl is the maximum voltage of a peak inverse voltage of		CO3- R	
	(a) V _{ml}	(b) $\sqrt{3} V_{ml}$	(c) $2V_{ml}$	(d) $3 V_{ml}$		
7.	In controlled converte current is decided by	er is feeding RL load	d, the ripple content of load		CO4- R	
	(a) Load resistance ald	one	(b) Load inductance alone	;		
	(c) Both R and L		(d) Both R and L			
8.	A four quadrant opera	tion requires			CO4- R	
	(a) Two full converter	rs in series				
	(b) Two full converters connected back to back					
	(c) Two full converters connected in parallel					
	(d) Two semi converters connected back to back					
9.	In a single phase full wave ac regulator using two thyristors only, the gate circuits of the two thyristors					
	(a) Must be isolated		(b) May or may not be iso	lated		
	(c) May or may not be	eisolated	(d) Should not be isolated			
10.	In single phase half wave regulator, the average current over one full cycle					
	(a) Is always positive		(b) May be positive or neg	gative		
	(c) May be positive or	negative	(d) Is always negative			
		PART - B (5	x 2= 10 Marks)			
11.	What is latching curre	ent of SCR?			CO1- R	
12.	Why thyristor are not	preferred for inverter	?		CO2- R	
13.	. List the applications of uncontrolled rectifiers.				CO3- R	
14.	Compare semi and ful	l converter.			CO4- R	
15.	Why is half wave AC	voltage regulator not	t used?		CO5- R	

$PART - C (5 \times 16 = 80 \text{ Marks})$

16. (a) Draw and explain the transfer, output and switching characteristics CO1 App (16) of MOSFET.

Or

- (b) With neat sketch explain the operation Boost converter with its CO1 App (16) waveform for the continuous and discontinuous current mode of operation.
- 17. (a) Construct a three phase inverter (180° conduction)and explain the CO2-App (16) operation with suitable waveform.

Or

- (b) Describe the three phase auto sequentially commutated CSI with CO2-App (16) power circuit. Draw the equivalent circuits and relevant waveforms.
- 18. (a) Analyze the working of a single phase half wave diode bridge CO3-Ana (16) rectifier feeding resistive load and determine the form factor and rectifier efficiency.

Or

- (b) Explain the working principle of three phase diode bridge rectifier CO3-Ana (16) with relevant diagram and wave form.
- 19. (a) Analyze the operation of single phase fully controlled bridge CO4- U (16) converter feeding RL load give corresponding circuit configuration and waveforms for continuous conduction mode.

Or

- (b) Derive the expressions for all external parameters measures of six CO4-Ana (16) pulse converters.
- 20. (a) Explain the operation of two stage sequence control of AC voltage CO5- U (16) controller.

Or

(b) Explain the operation of TRIAC based single phase full wave AC CO5- U voltage controller. (16)