

Reg. No. :

--	--	--	--	--	--	--	--	--	--

Question Paper Code: 34202

B.E. / B.Tech. DEGREE EXAMINATION, NOV 2019

Fourth Semester

Computer Science and Engineering

01UCS402 - SOFTWARE TESTING

(Regulation 2013)

Duration: Three hours

Maximum: 100 Marks

Answer ALL Questions.

PART A - (10 x 2 = 20 Marks)

1. Differentiate verification and validation.
2. List any two classification of defect.
3. What is the purpose of negative testing?
4. List the methods of White box testing.
5. Define test harness.
6. List out the types of system testing.
7. What are business impacts of globalization?
8. List the importance of test plan.
9. Define Defect Removal Leverage (DRL).
10. Define test measurement process.

PART - B (5 x 16 = 80 Marks)

11. (a) Write and explain the Software testing principles in detail. (16)

Or

(b) Explain the origin of defect and its classes with its repository in detail. (16)

12. (a) What is Equivalence class partitioning? List rules used to define valid and invalid equivalence classes. Explain the technique using example. (16)

Or

(b) (i) Explain the various iterations of Loop testing. (8)

(ii) Write a note on Evaluating Test adequacy Criteria. (8)

13. (a) Describe the activities or task and responsibilities for developer or tester in support of multilevel testing. (16)

Or

(b) Explain in detail System and validation Testing and all its types. (16)

14. (a) Write any four IEEE recommended test related documents in detail. (16)

Or

(b) What role does user/client play in the development of test plan for a project? Should they be present at any of the test plan reviews? Justify your answer with a case study. (16)

15. (a) Explain the five stop-test criteria that are based on quantitative approach. (16)

Or

(b) Classify the different types of review and discuss in detail the components of review plans. (16)
