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**Question Paper Code: 41353**

B.E. / B.Tech. DEGREE EXAMINATION, MAY 2017

Fifth Semester

Electrical and Electronics Engineering

14UEE503 - POWER SYSTEM ANALYSIS

(Regulation 2014)

Duration: Three hours

Maximum: 100 Marks

Answer ALL Questions

PART A - (10 x 1 = 10 Marks)

- The bus matrix of power system is not a
  - symmetric
  - square
  - full matrix
  - having dominant diagonal element
- The value of diversity factor is
  - 0
  - 1
  - less than 1
  - greater than 1
- Which among the following buses constitute the maximum number in a power system?
  - Slack bus
  - P Q bus
  - P V bus
  - All of these
- Gauss Seidal iterative method can be used to solve a set of
  - linear differential equation only
  - linear and non linear algebraic equations
  - linear and non linear differential equations
  - linear algebraic equation only
- If the P.U value of synchronous impedance is 2, what is short circuit ratio?
  - 0.05
  - 0.5
  - 2
  - 0.02



(delta / star) : 30 MVA, 11/220 kV,  $X = 15\%$

(16)

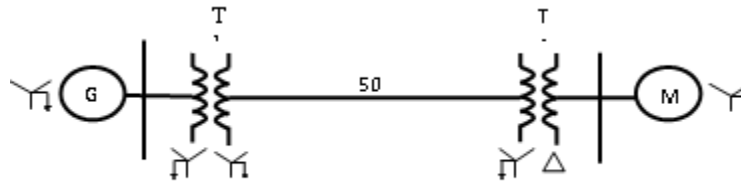


Figure 1

Or

- (b) Explain the modelling of generator, load, transmission line and transformer for power flow, short circuit and stability studies. (16)
17. (a) Explain the algorithm of Gauss Seidal method for the load flow problem with a neat flow chart and relevant equations. (16)

Or

- (b) Clearly the algorithmic steps for solving load flow equations using Newton – Raphson method (polar form) when the system contains all types of buses. Assume that the generators at the P-V buses have adequate Q- limits. (16)
18. (a) A 11 kV, 100 MVA alternator having a sub-transient reactance of 0.25 pu is supplying to a 50 MVA motor having a sub-transient reactance of 0.2 pu through a transmission line. The line reactance is 0.05 pu on a base of 100 MVA. The motor is drawing 40 MW at 0.8 p.f. leading with a terminal voltage of 10.95 kV when a 3-phase fault occurs at the generator terminals. Calculate the total current in generator and motor under fault conditions. (16)

Or

- (b) Explain the step by step procedure for systematic fault analysis for three phase fault using bus impedance matrix. (16)
19. (a) Draw the sequence network connection for LL fault at any point in a power system. From that obtain an expression for the fault current. (16)

Or

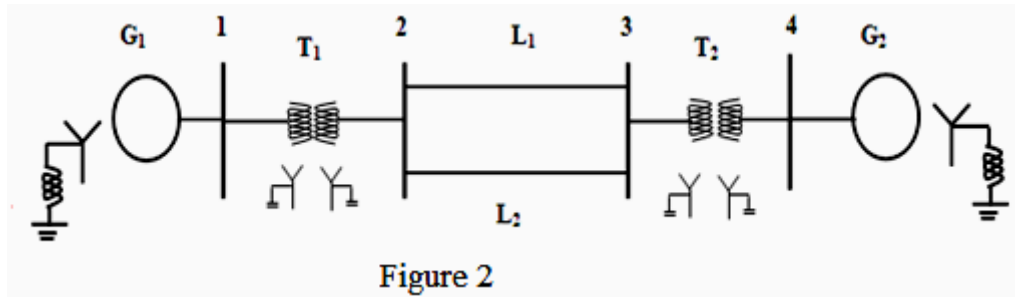
- (b) The one-line diagram of a simple power system is shown in figure 2 below. Determine the fault current and fault MVA when

- (i) A double line to ground fault occurs at bus 4
- (ii) A single line to ground fault occurs at bus 4

$G_1, G_2 : 100MVA, 20kV, x_+ = x_- = x_d'' = 20\% ; x_0 = 4\% ; x_n = 5\%$

$T_1, T_2 : 100MVA, 20kV/345kV ; x_{leak} = 8\%$

$L_1, L_2 : x_+ = x_- = 15\% ; x_0 = 50\%$  on a base of 100MVA (16)



20. (a) Derive swing equation for a single machine connected to infinite bus system. State the usefulness of this equation. State the reasons for non linearity of this equation. (16)

Or

- (b) State and explain equal area criterion in connection with transient stability analysis. What are the advantages and limitations of this method. (16)