Reg. No.:					

Question Paper Code: 41455

B.E. / B.Tech. DEGREE EXAMINATION, MAY 2017

Fifth Semester

Electronics and Communication Engineering

14UEC505 - MICROPROCESSORS, MICROCONTROLLERS AND APPLICATIONS

(Regulation 2014)

Duration: Three hours Maximum: 100 Marks

Answer ALL Questions

	PA	ART A - $(10 \times 1 = 10 \text{ Marks})$				
1.	List out 16 bit Registers					
	(a) Accumulator	(b) Stack pointer				
	(c)BC Register	(d)All the above				
2.	Why is 8085 processor called as 8 bit processor?					
	(a) Has 8 bit ALU	(b) Has 8 bit Data bus				
	(c) None of these	(d) Both (a) and (b)				
3.	Which bus is bidirectional?					
	(a) Address bus	(b) Control bus				
	(c) Data bus	(d) None of these				

- 4. Which microprocessor has multiplexed data and address lines?
 - (a) 8086 (b) 8085 (c) 8051 (d) Pentium
- 5. Which group of instructions does not affect the flags?
 - (a) Arithmetic operations(b) Logic operations(c) Data transfer operations(d)Branch operations

6.	In 8086 micropro	cessor the followin	g has the highest pr	iority among all type into	errupts?			
	(a) NMI	(b) DIV 0	(c) TYPE 255	(d) OVER FLOW				
7.	What is SJMP?							
	(a) Short Jum (c)Synchroniz	_	-	(b)Stack Jump(d) State Jump				
8.	When 8051 wake	s up then 0x00 is lo	paded to which regis	ster?				
	(a) DPTR (c) PC		(b) Stack point (d) PSW	(b) Stack pointer(d) PSW				
9.	If we push data or	nto the stack then t	he stack point					
	(a) increases (c) none of the	with every push e above	` '	(b) decreases with every push(d) increases with only push				
10.	How many 16 bit	registers are availa	able in 8051?					
	(a) 1	(b) 2	(c) 3	(d) none of these				
		PART -	B $(5 \times 2 = 10 \text{ Mark})$	s)				
11.	Explain various ty	pes of Instructions	s used in 8085.					
12.	Draw the HOLD	response timing cy	cle in Minimum mo	de of 8086.				
13.	Draw the block di	agram of Program	mable Interrupt Con	troller (8259).				
14.	Write an ALP for	adding of two num	nbers and store the r	esult in 2050H in 8051.				
15.	Give an application	on for Sensor based	d 8051 Microcontrol	ler.				
		PART -	$C (5 \times 16 = 80 \text{ Mark})$	cs)				
16.	(a) Define Interru	upts .What are its v	rarious types? How a	are Interrupts operated in	n 8085. (16)			
			Or					
	(b) Write an assuring 8085.	embly language p	rogram for Sorting	of Numbers in ascend	ling order (16)			
17.	(a) With a neat sl	xetch draw and exp	plain the Internal arc	hitecture of 8086.	(16)			
			Or					

	(b)	(i) Discuss the various Addressing Modes of 8086.	(8)
		(ii) List out the different types of Instruction used in 8086.	(8)
18.	(a)	With the help of a neat diagram explain DMA Controller.	(16)
		Or	
	(b)	With the help of a neat diagram explain Programmable Interrupt Timer(8253/	(8254) (16)
19.	(a)	Discuss 8051 Microcontroller Hardware Architecture with a neat diagram.	(16)
		Or	
	(b)	Brief about 8051 Microcontroller ports in detail.	(16)
20.	(a)	Describe the Analog to Digital Conversion (ADC) Interfacing with 8051.	(16)
		Or	
	(b)	How does Washing Machine Operated in interfacing with 8051?	(16)